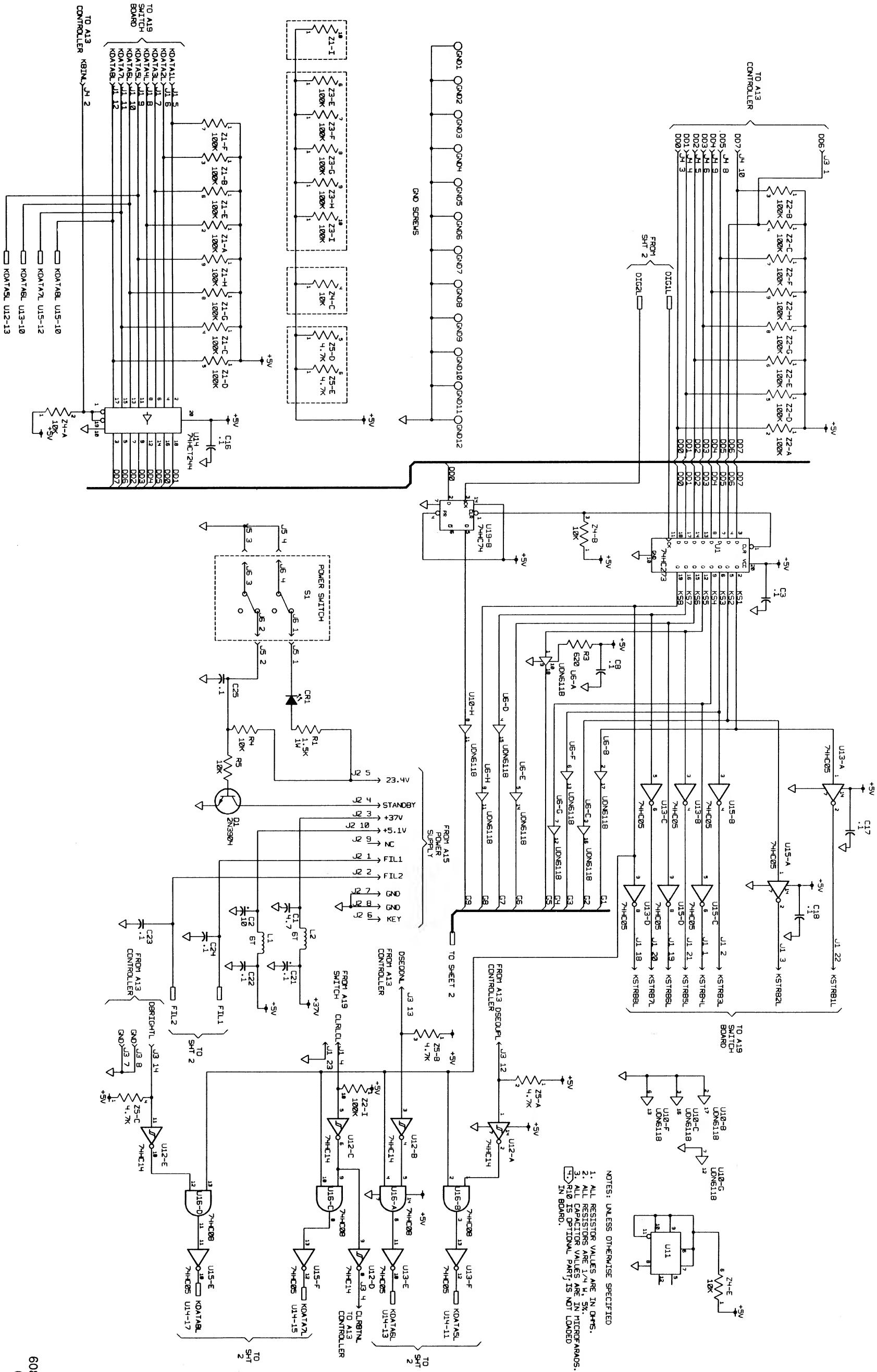


Figure 8-1. A1 Display PCA

SCHEMATIC DIAGRAMS



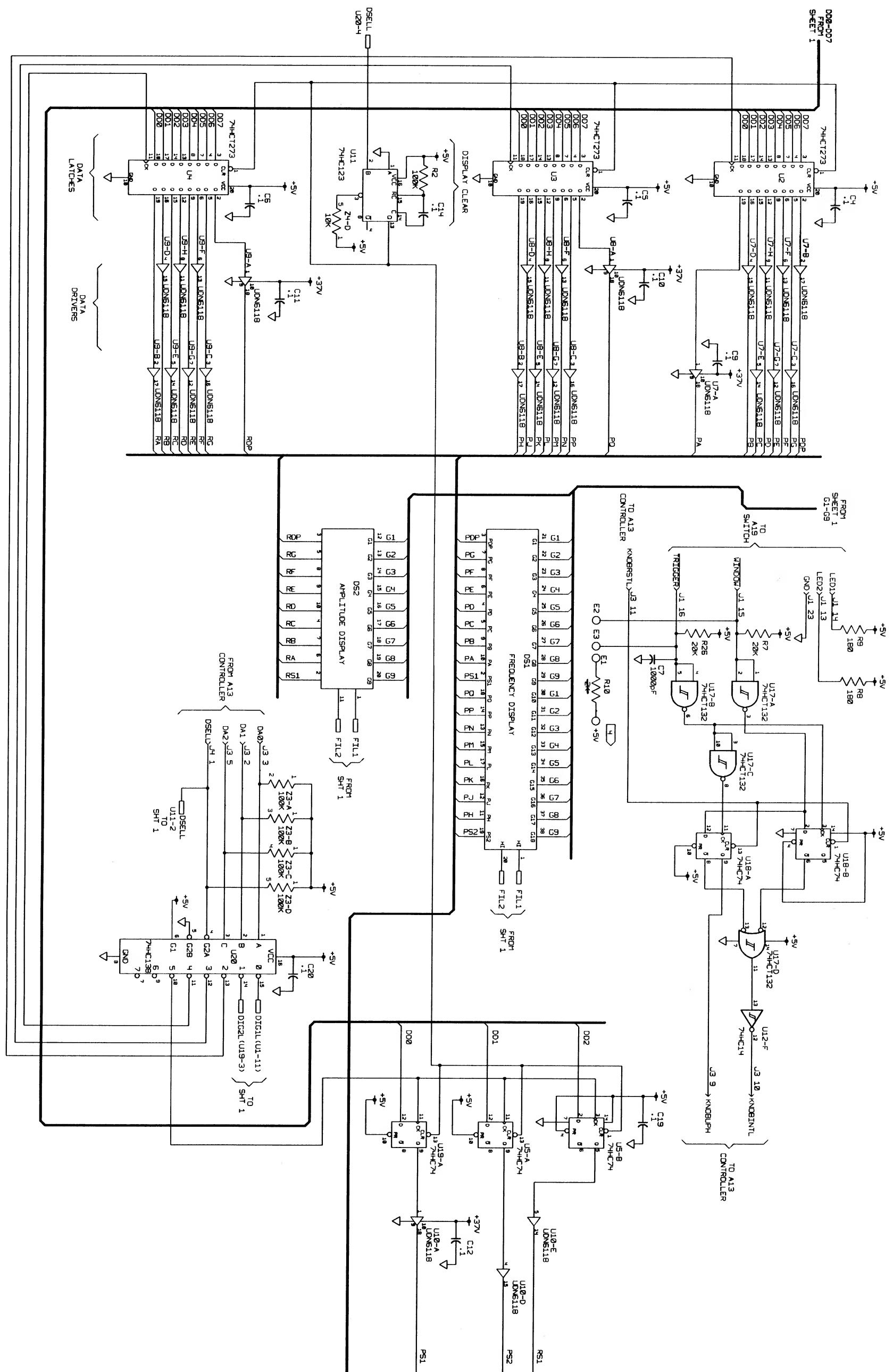
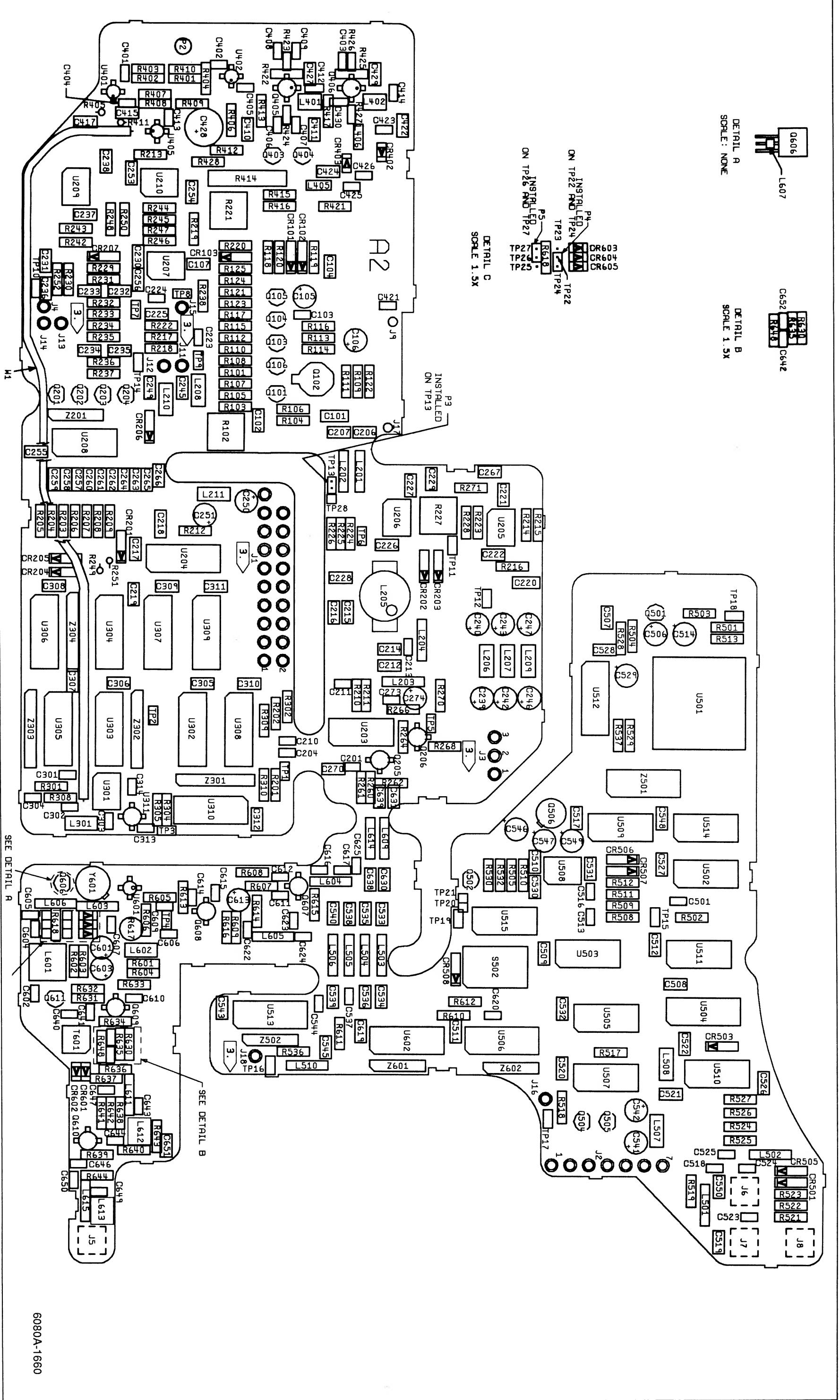


Figure 8-1. A1 Display PCA (cont)

6080A-1050
(2 of 2)

Schematic Diagrams



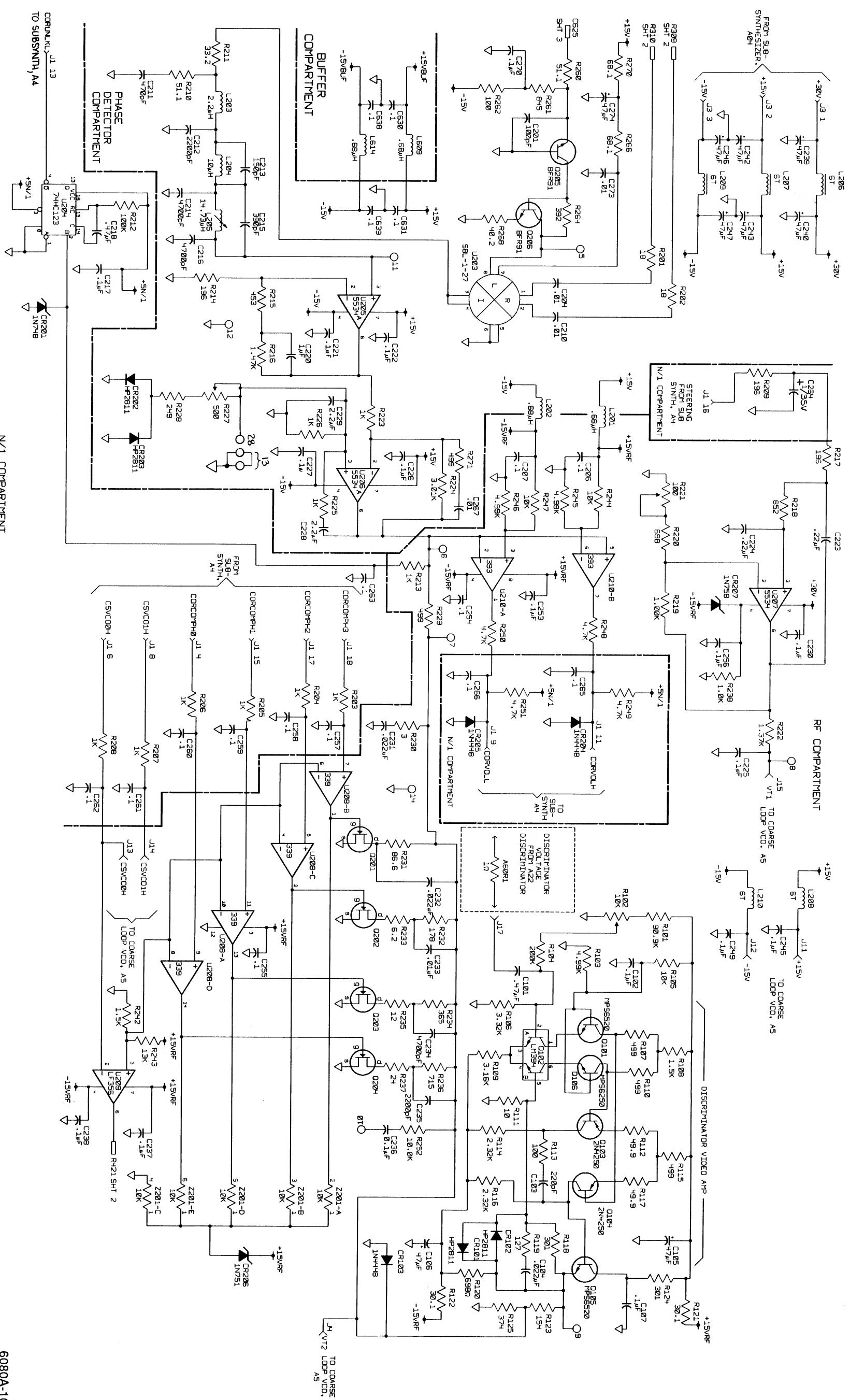


Figure 8-2. A2 Coarse Loop PCA (cont)

SCHEMATIC DIAGRAMS

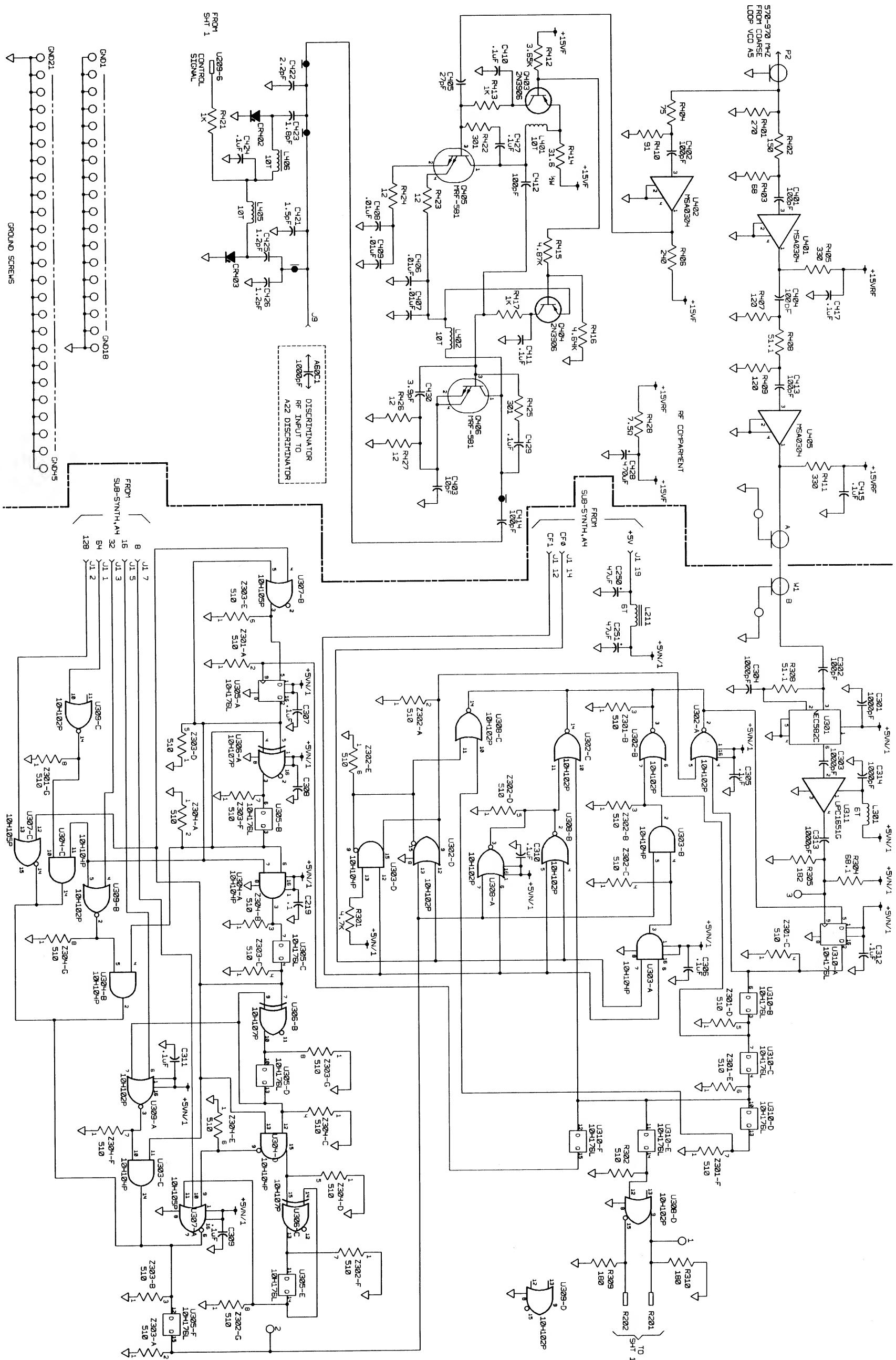


Figure 8-2. A2 Coarse Loop PCA (cont)

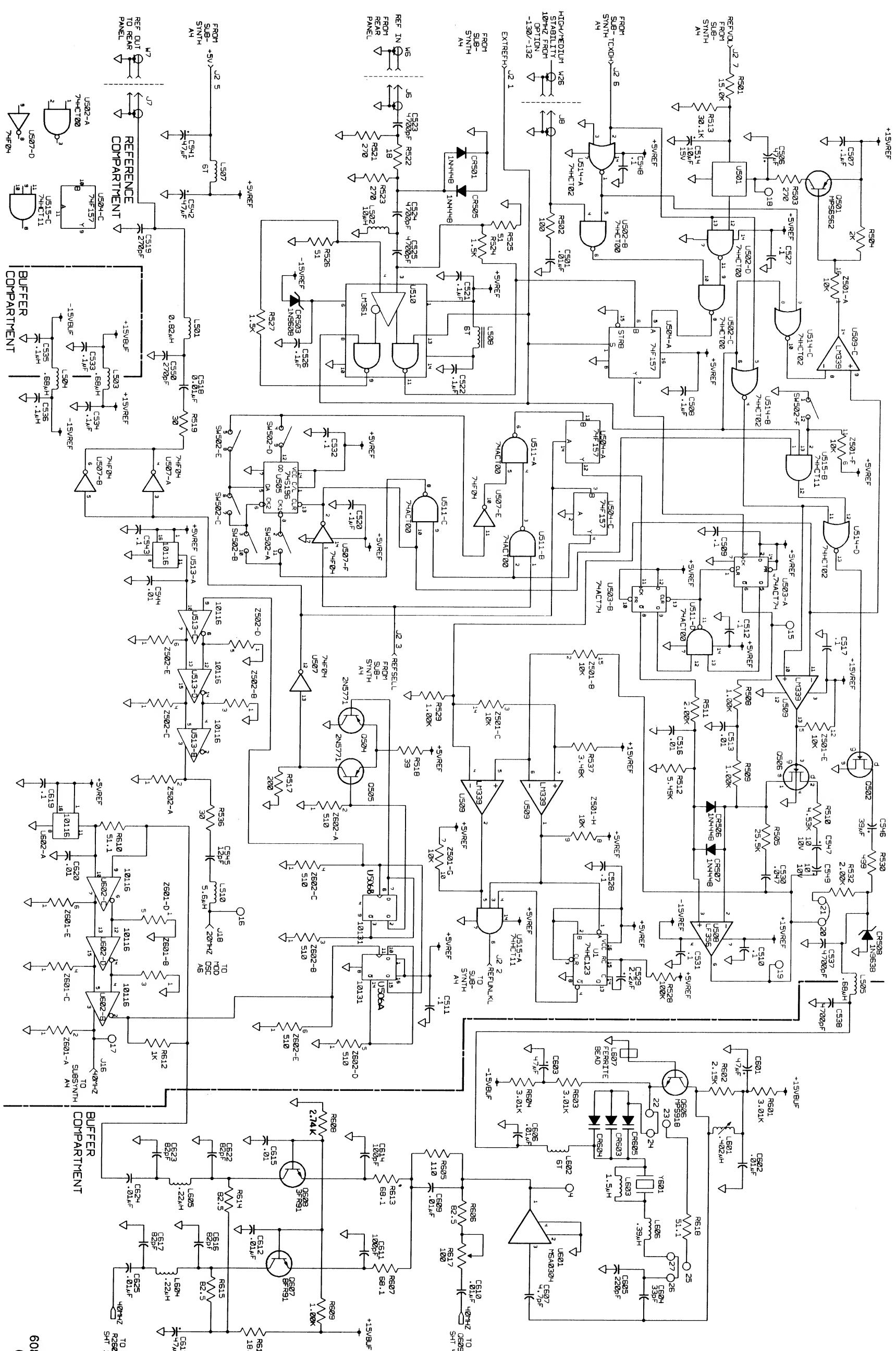


Figure 8-2. A2 Coarse Loop PCA (cont)

6080A-1060
(3 of 4)

Schematic Diagrams

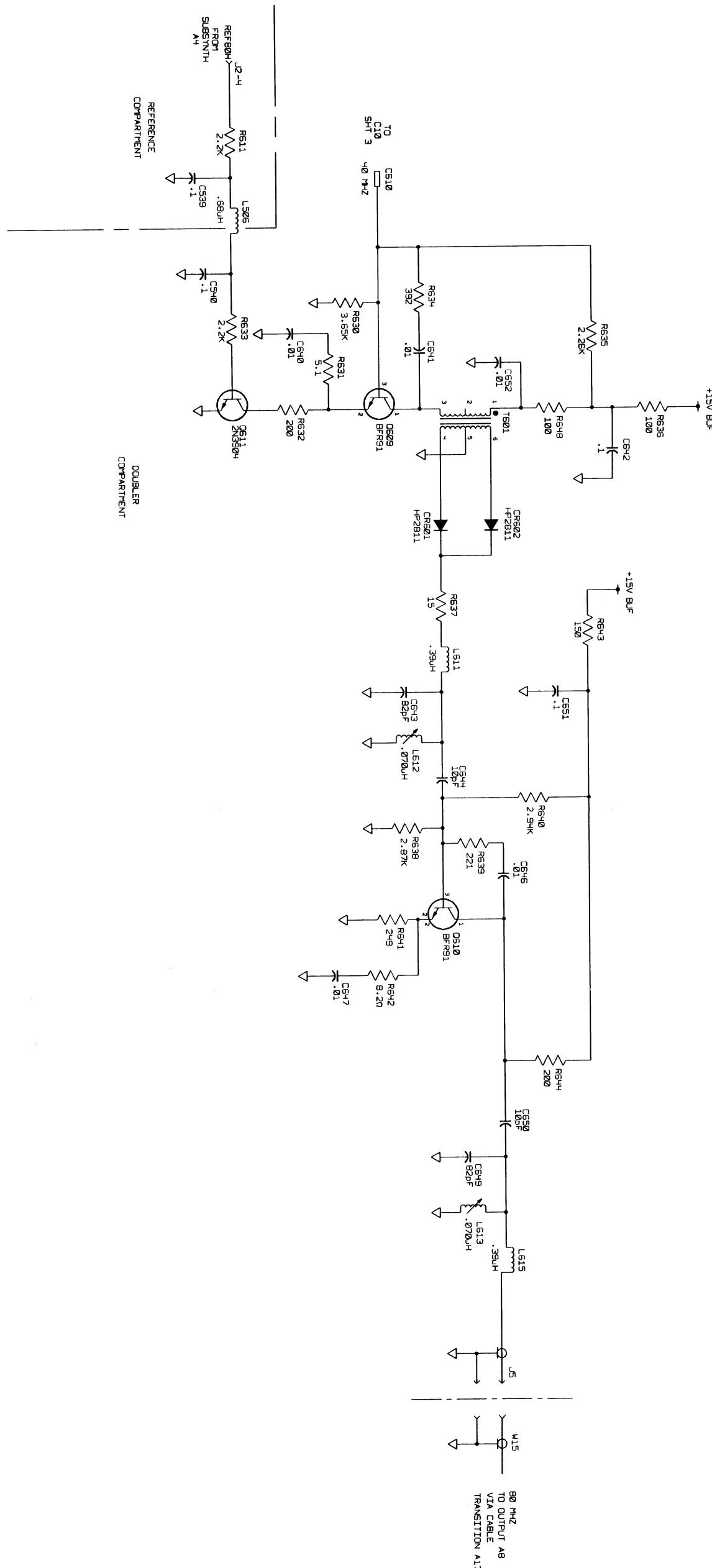
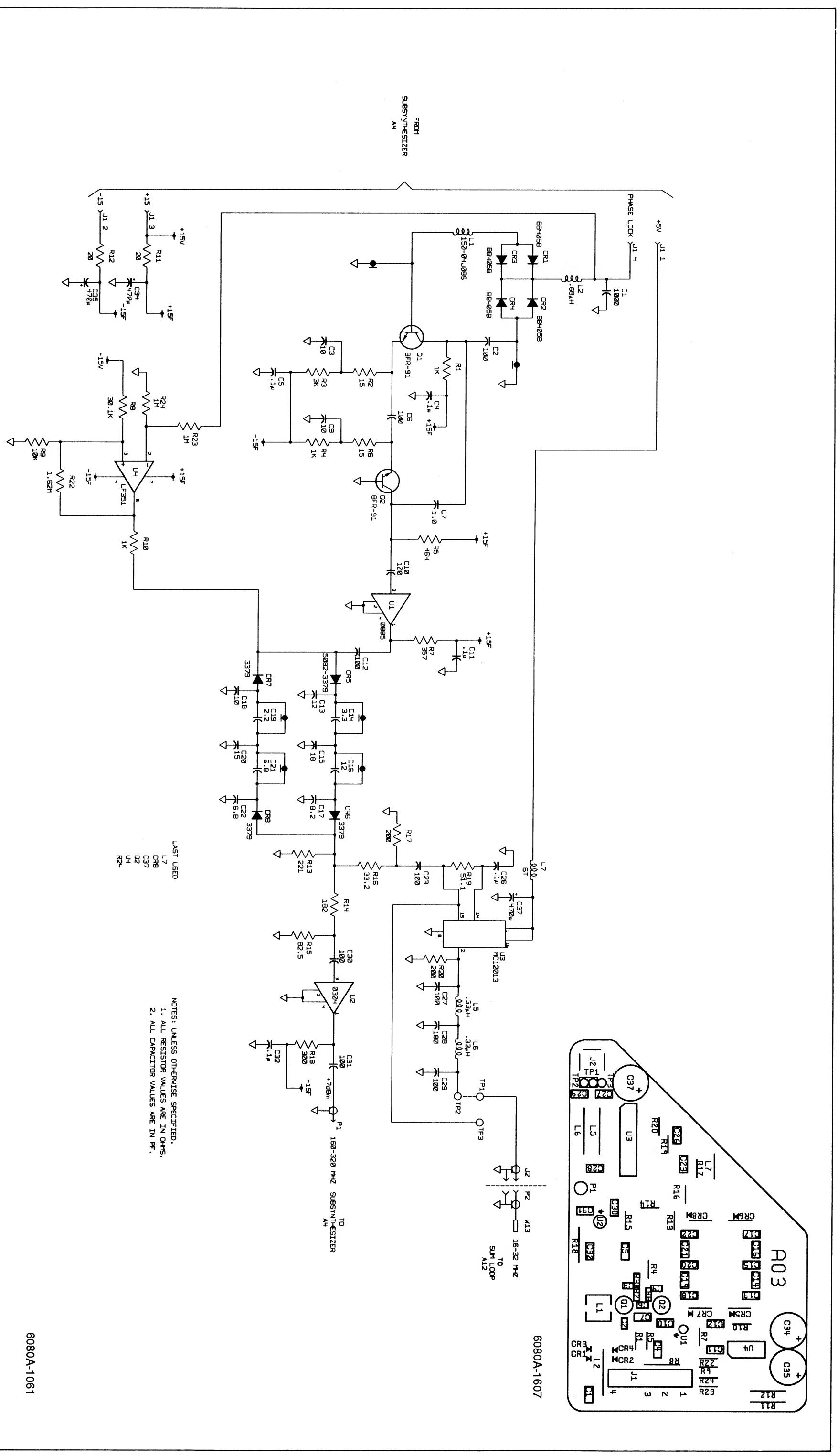


Figure 8-2. A2 Coarse Loop PCA (cont)



SCHEMATIC DIAGRAMS

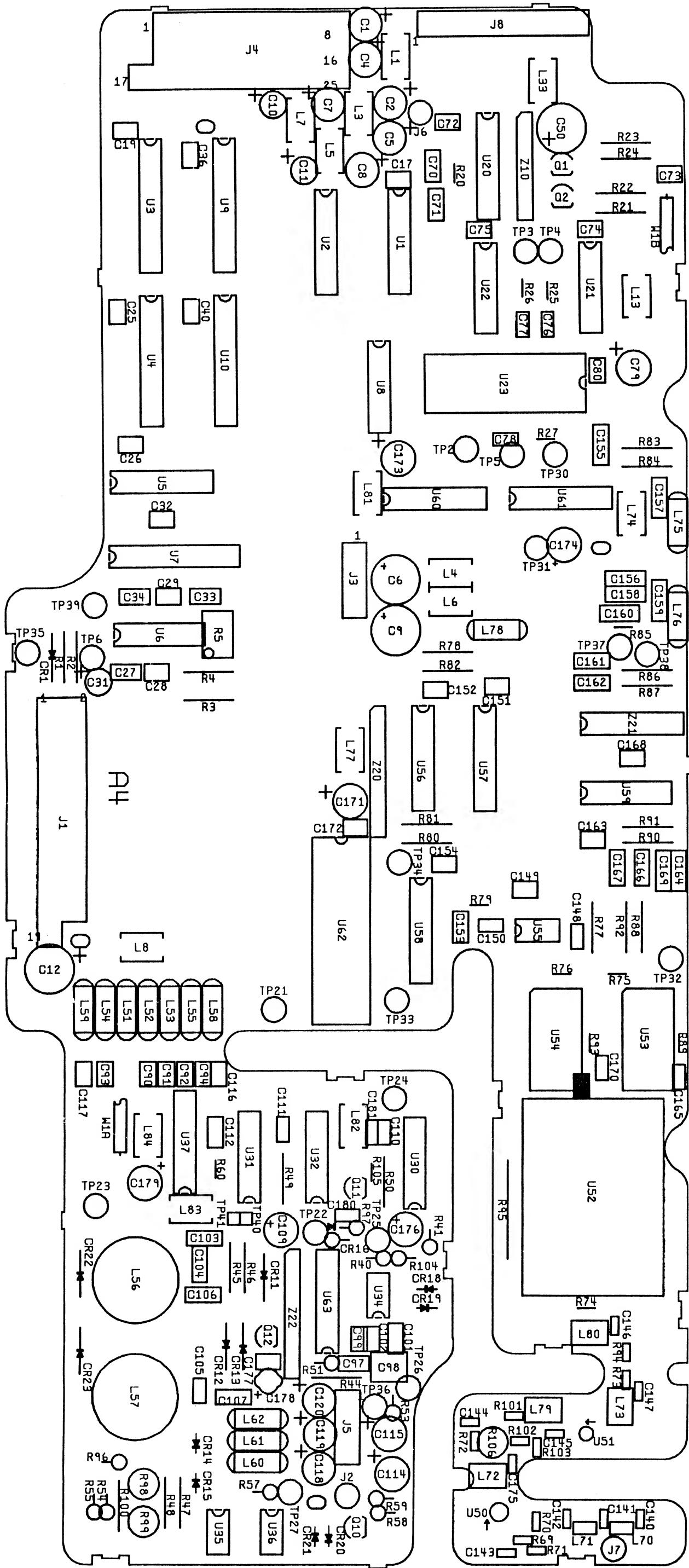
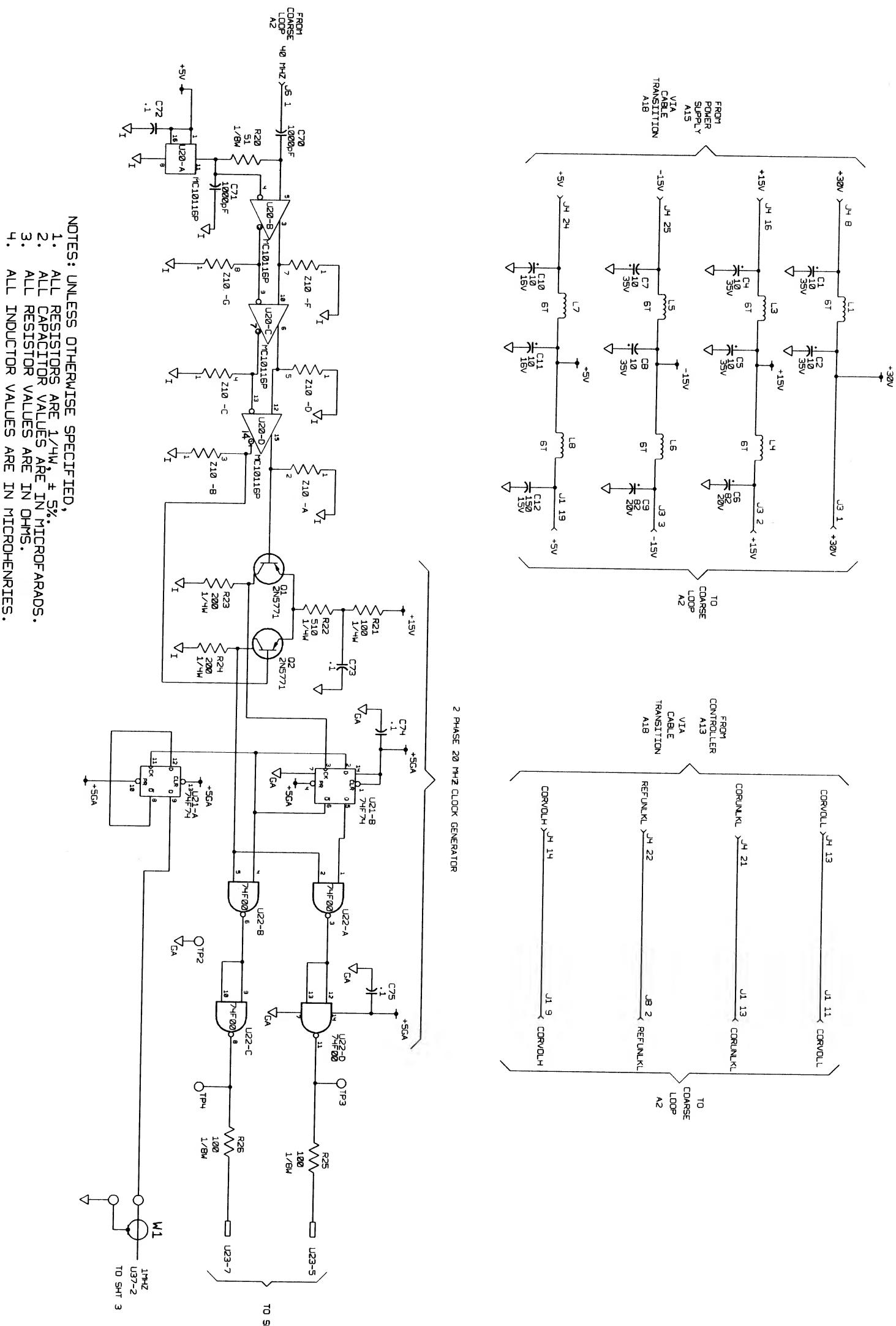


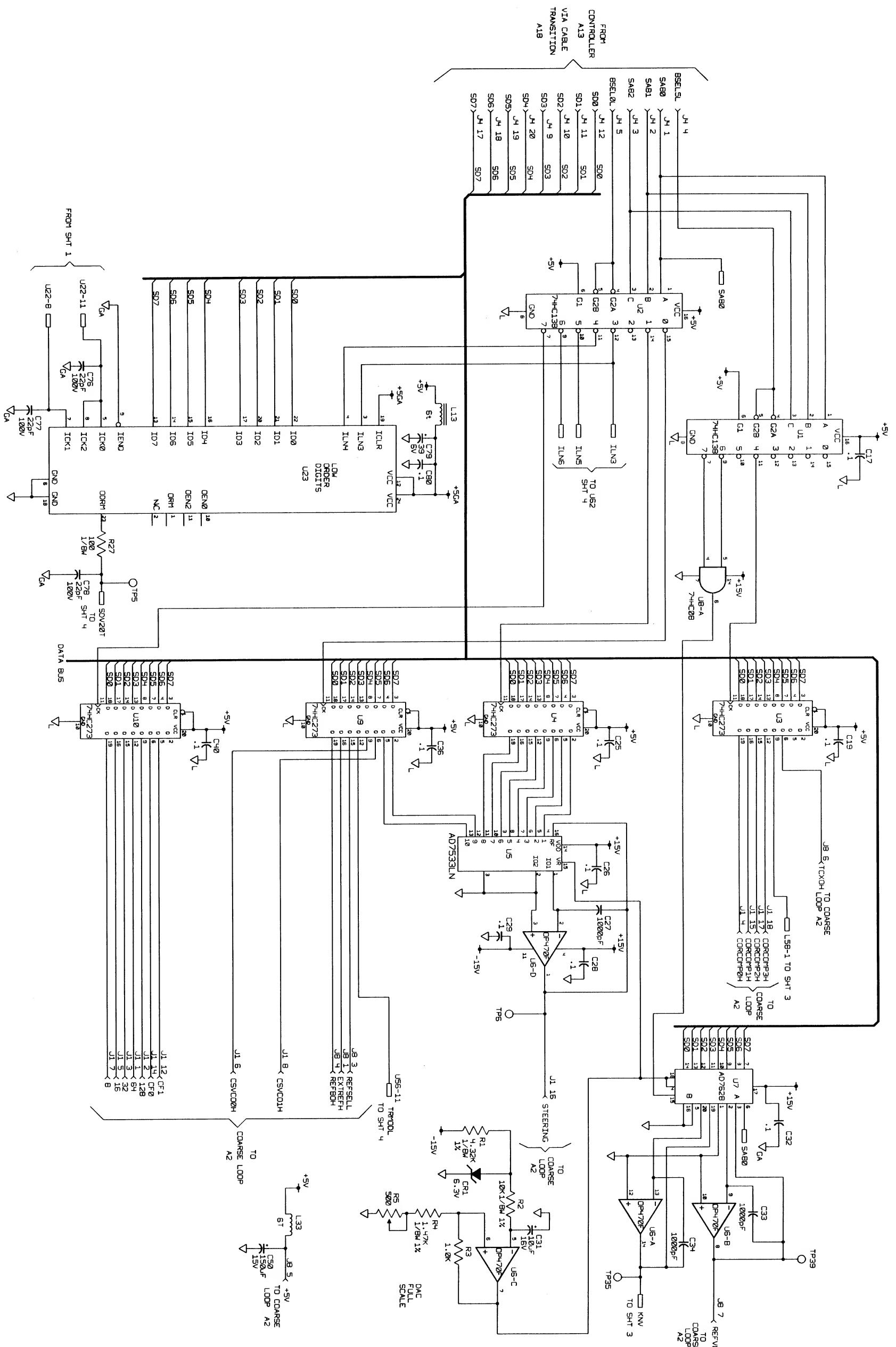
Figure 8-4. A4 Sub-Synthesizer PCA



NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTORS ARE $1/4W$, $\pm 5\%$.
 2. ALL CAPACITOR VALUES ARE IN MICROFARADS.
 3. ALL RESISTOR VALUES ARE IN OHMS.
 4. ALL INDUCTOR VALUES ARE IN MICROHENRIES.

Figure 8-4. A4 Sub-Synthesizer PCA (cont)

SCHEMATIC DIAGRAMS



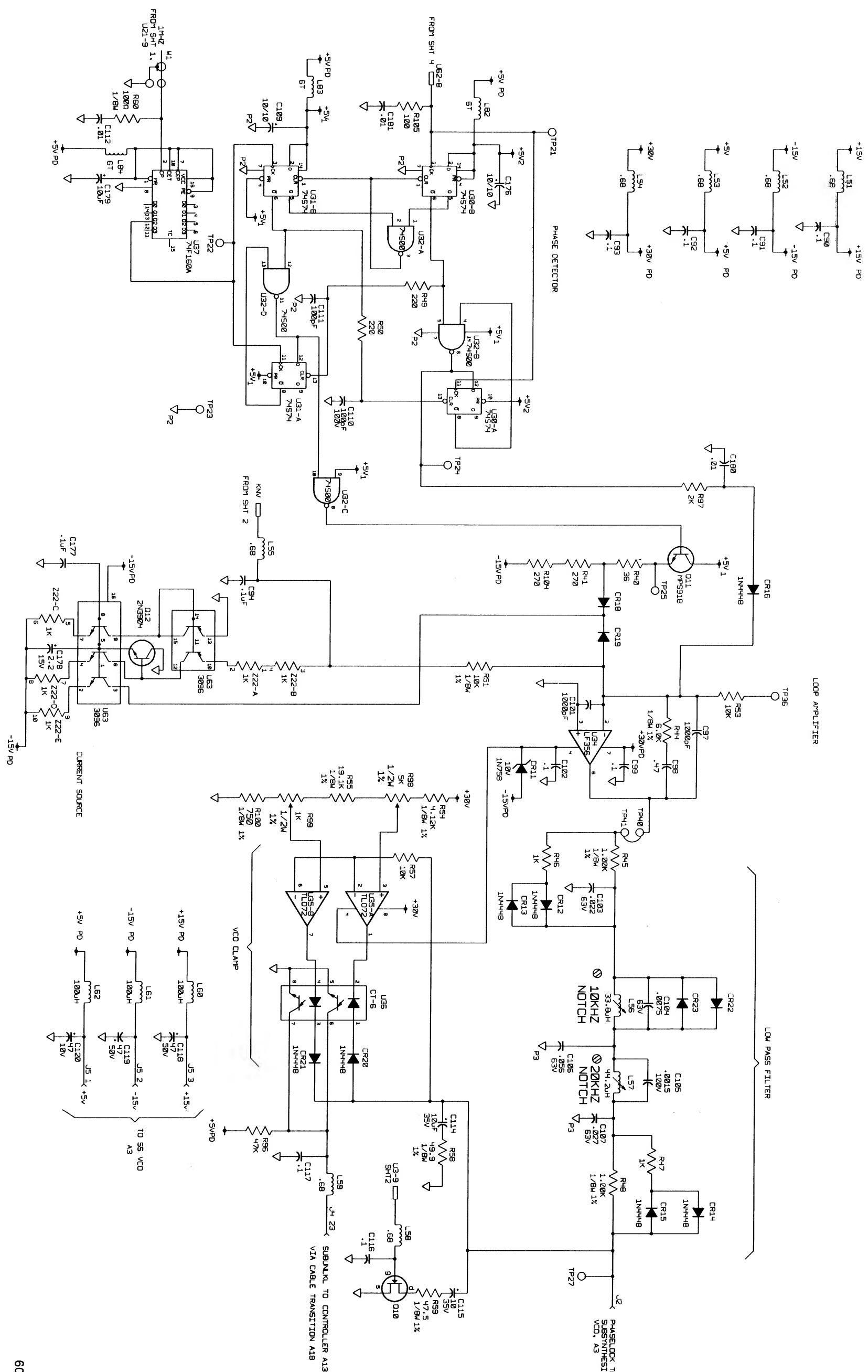
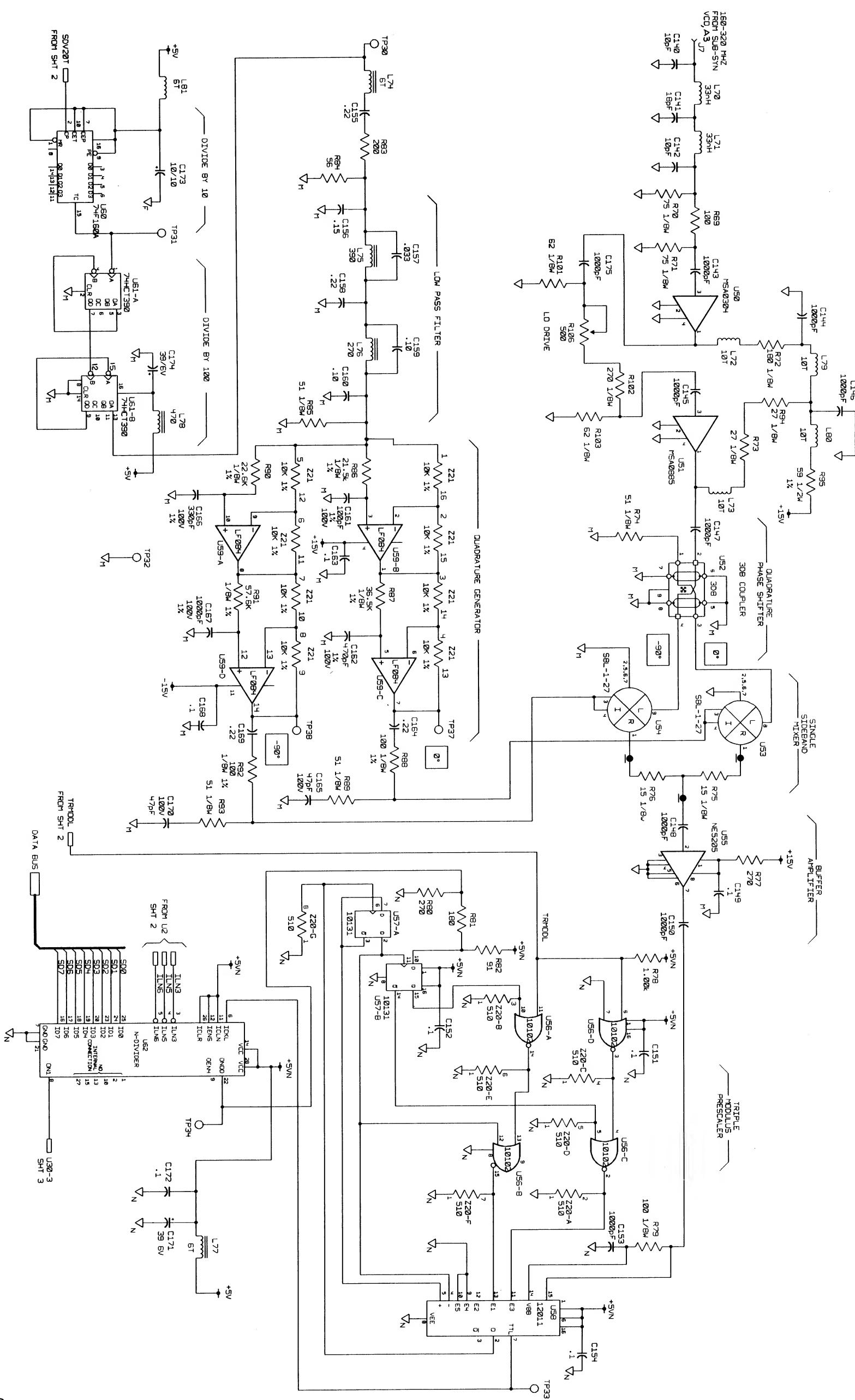


Figure 8-4. A4 Sub-Synthesizer PCA (cont.)

6080A-1062
(3 of 4)

Schematic Diagrams

Figure 8-4. A4 Sub-Synthesizer PCA (cont)



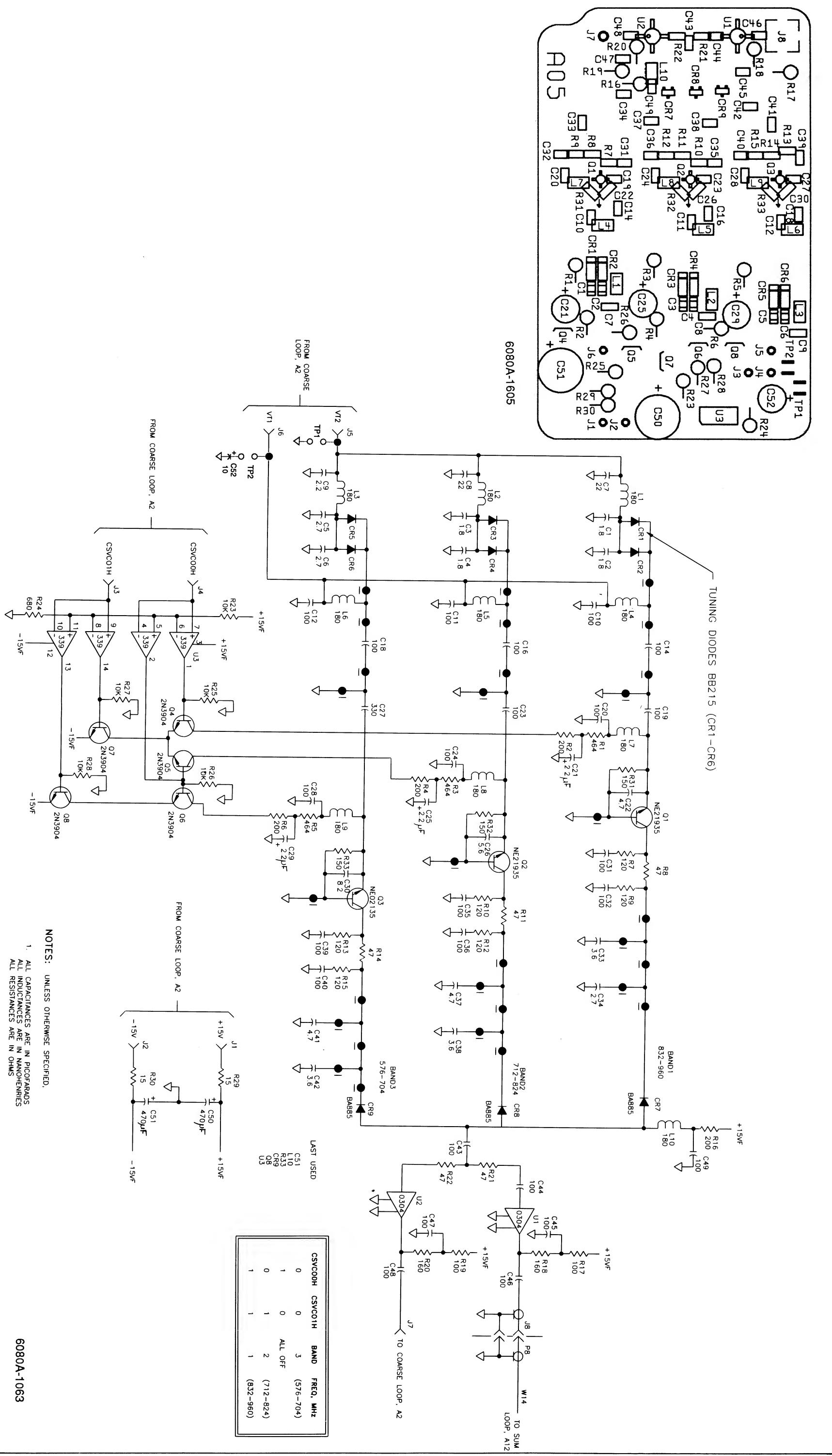
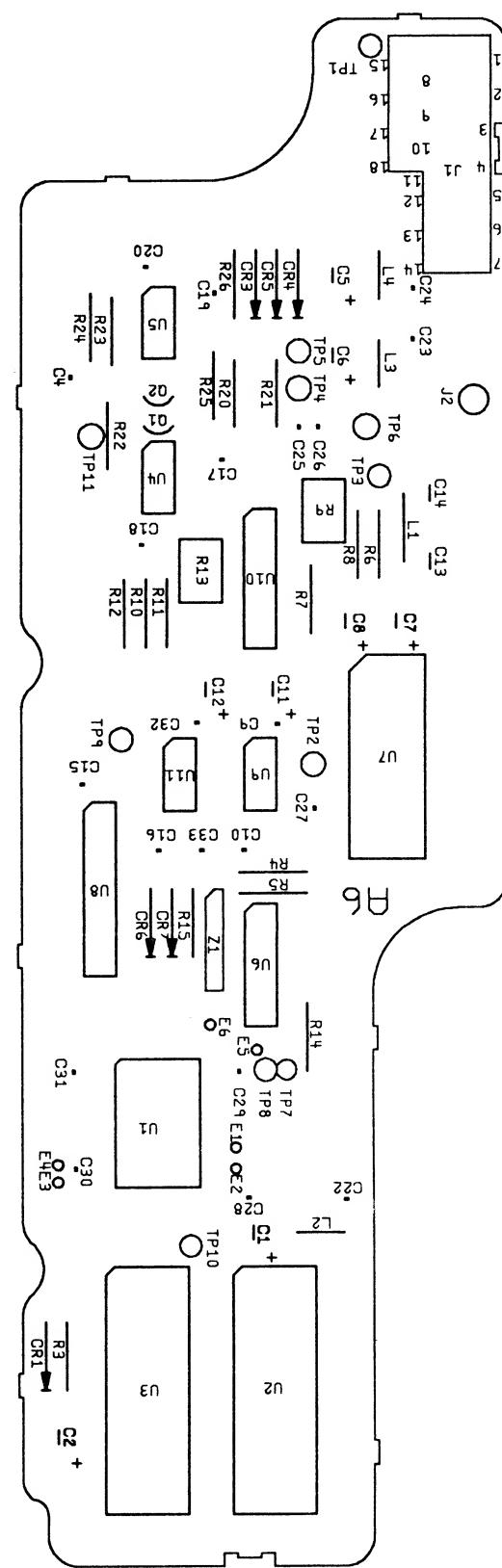


Figure 8-5. A5 Coarse Loop VCO PCA

SCHEMATIC DIAGRAMS



6080A-1602

REF.	V _{CC}	+15V	-15V	PINS	TYPE
U1	2,23,37 42,63	3,22,33, 43,62	80	65022 ARRAY	
U2	1,28	14	28	27256	
U3	1,28	14	28	27256	
U4	7	4	8	LM361	
U5	7	4	8	LM361	
U6	3	12	14	LM339	
U7	3	7	12	AD565A	
UB	18	3	2	20	AD754B
U9	8	4	8	LF412A	
U10	13	4	5	16	D6308
U11	8	4	8	LF412A	

| PB | PC | MODE | S1 | S2 | S3 | S4 | S5 | Operation:

PB<6> PB<5> PB<4>		Waveform selected:	
1	0	0	Sine Wave
1	1	0	Triangular Wave
1	1	1	Square Wave
1	0	1	Gaussian noise
0	0	0	Smooth Wave
0	0	1	4 step Staircase
0	1	0	Option Wave
1	1	0	INT MOD is on
1	1	1	option wave

NOTE: (unless otherwise specified)

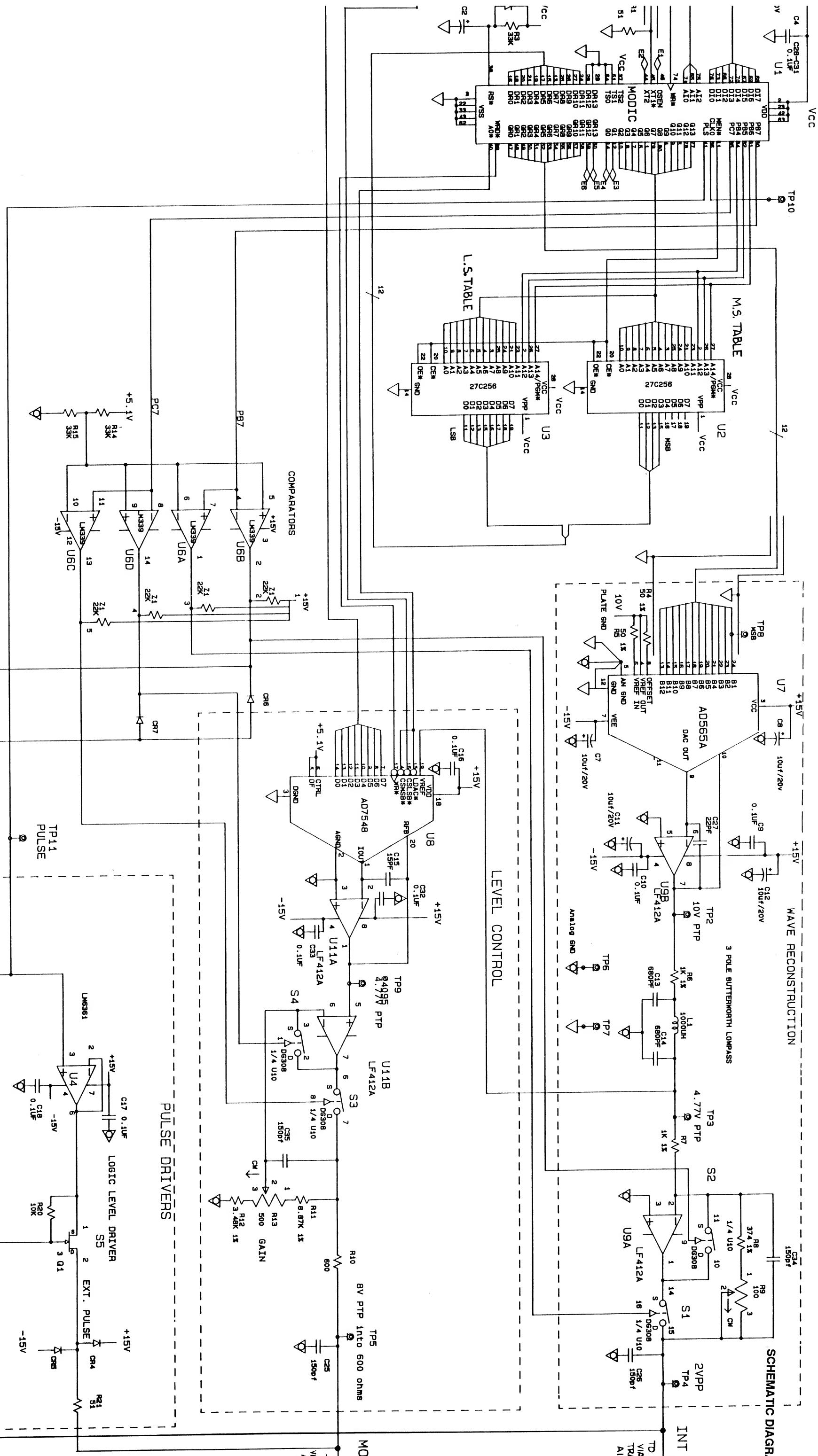
- 1) All capacitor values are in μF .
- 2) All resistor values are in Ohms.

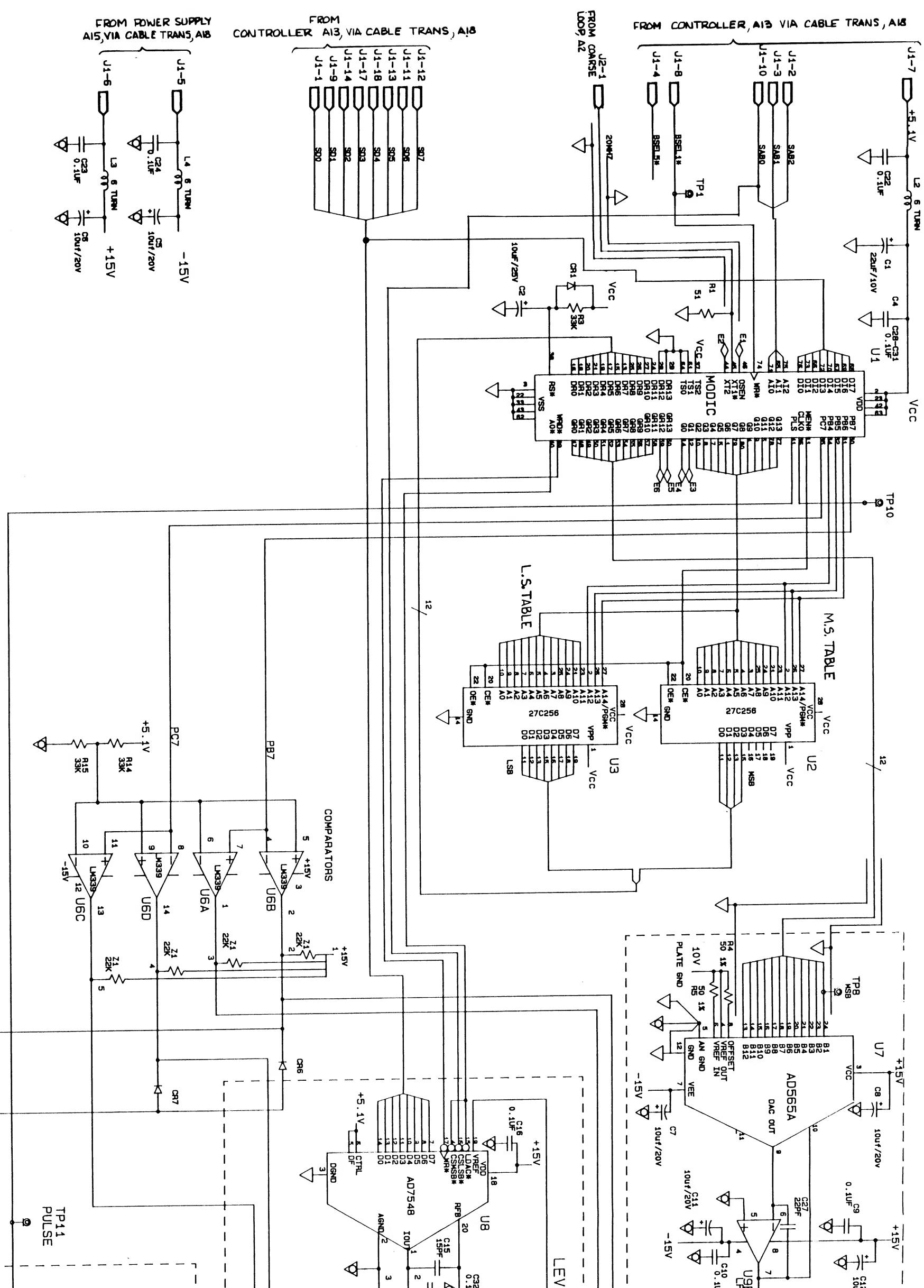
PB<6>	PB<5>	PB<4>	S1	S2	S3	S4	S5	Operation:
0	0	0	0	0	0	0	0	Pulse generation
1	1	1	1	0	0	0	0	MOD OUT and INT MOD are on
1	1	1	1	0	0	0	0	MOD OUT and INT MOD are FULL scale
1	0	0	1	0	0	0	0	MOD OUT and INT MOD are ZERO scale
0	1	0	1	0	0	0	0	MOD OUT is on
0	1	0	1	0	0	0	0	INT MOD is on
0	1	0	0	0	0	0	0	INT MOD is on

Sn - refers to switches on the PCB.

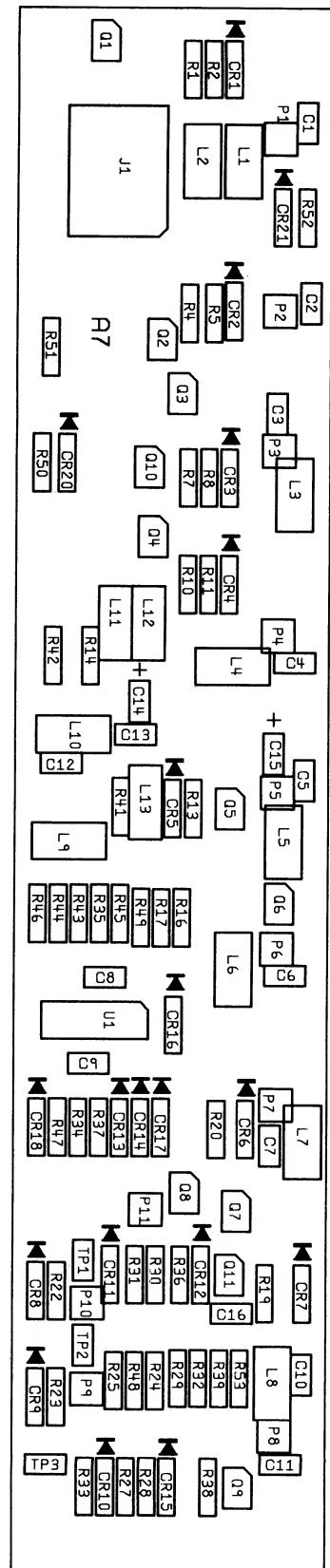
6080A-1067
(1 of 2)

Figure 8-6. A6 Mod Oscillator PCA





SCHEMATIC DIAGRAMS



6080A-1604

TO
A21
ATTENATOR/
RPP

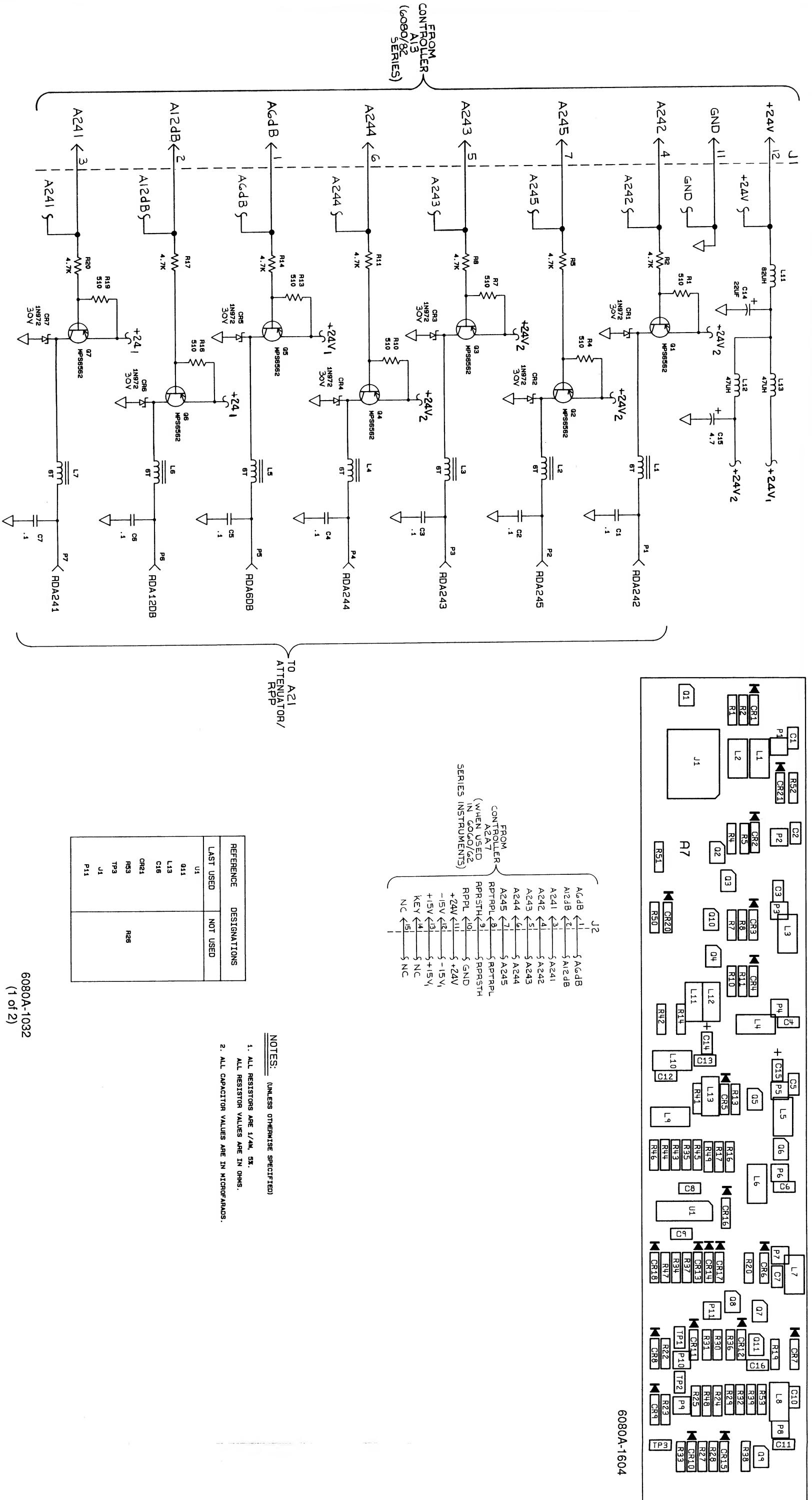
NOTES: (UNLESS OTHERWISE SPECIFIED)

1. ALL RESISTORS ARE 1/4W, 5%.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS.

REFERENCE	DESIGNATIONS
LAST USED	NOT USED

6080A-1032
(1 of 2)

Figure 8-7. A7 Relay Driver PCA



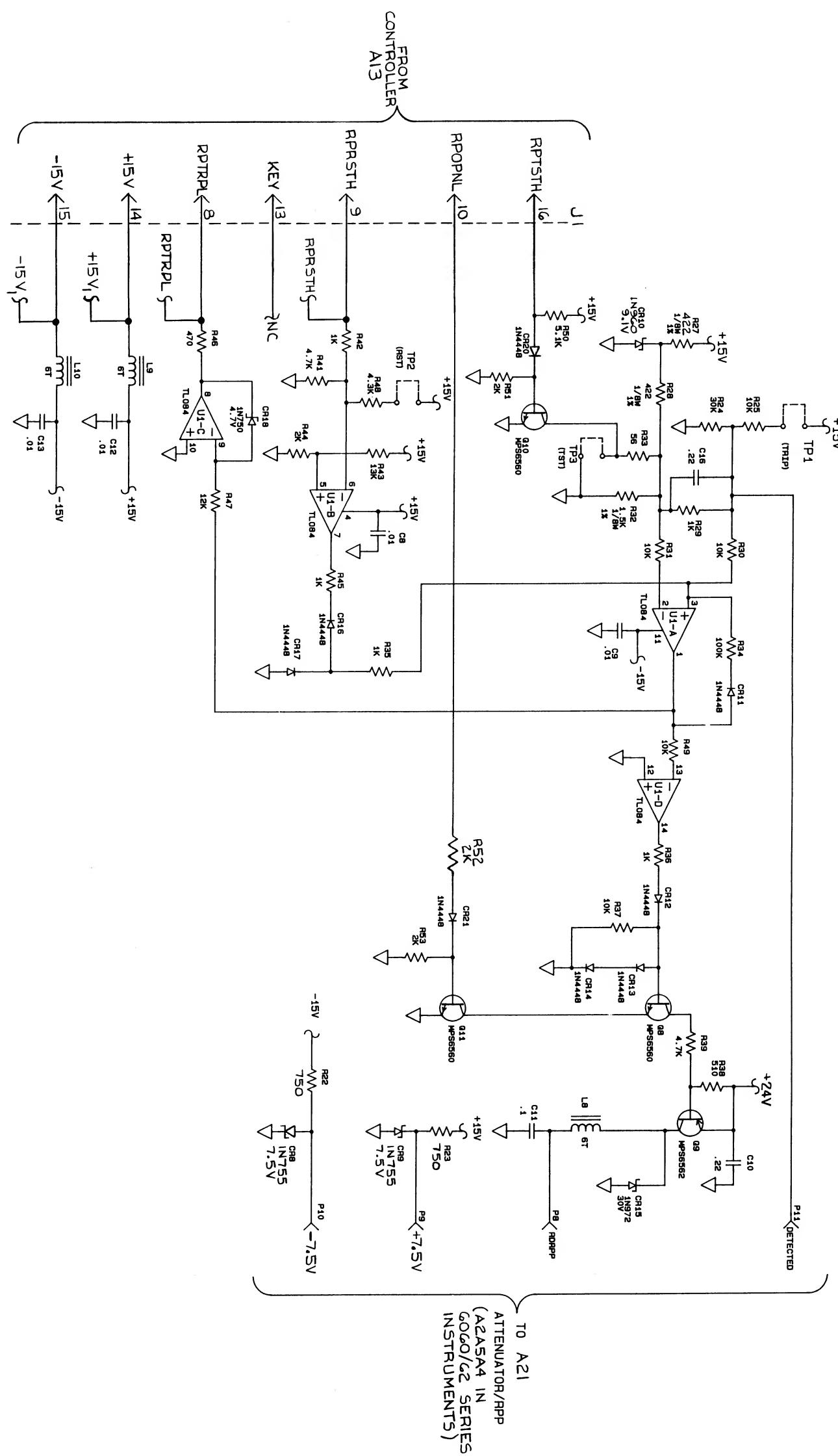


Figure 8-7. A7 Relay Driver PCA (cont)

6080A-1032
(2 of 2)

SCHEMATIC DIAGRAMS

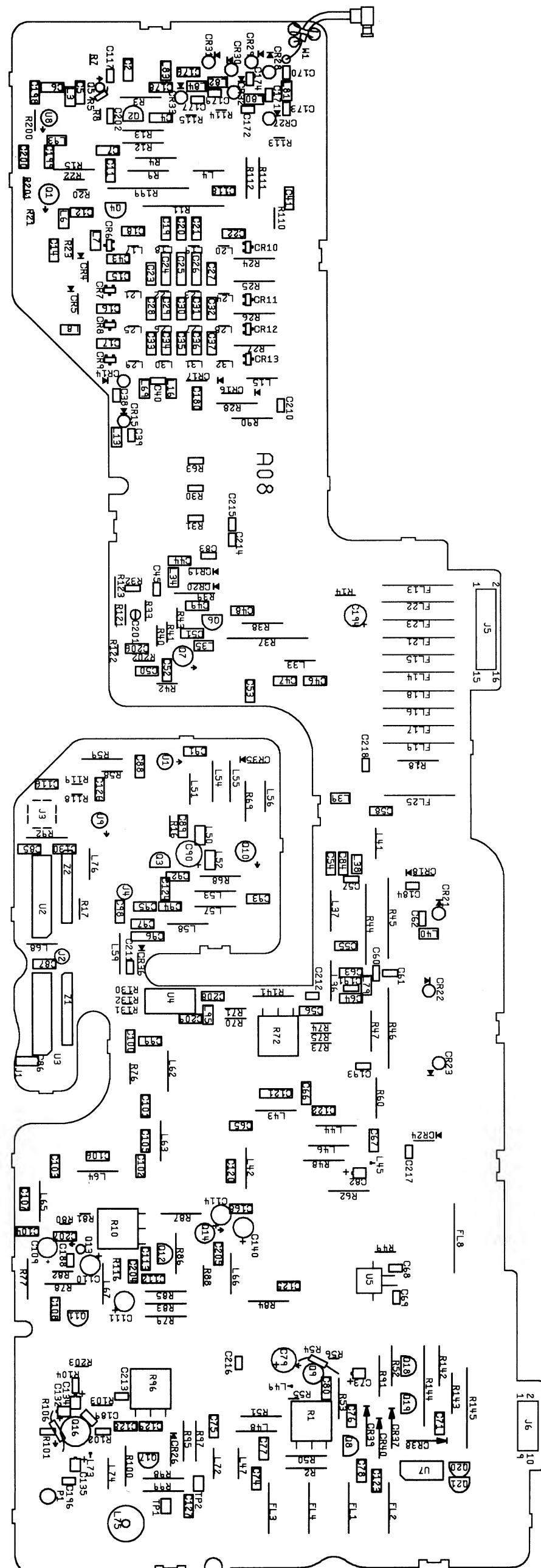
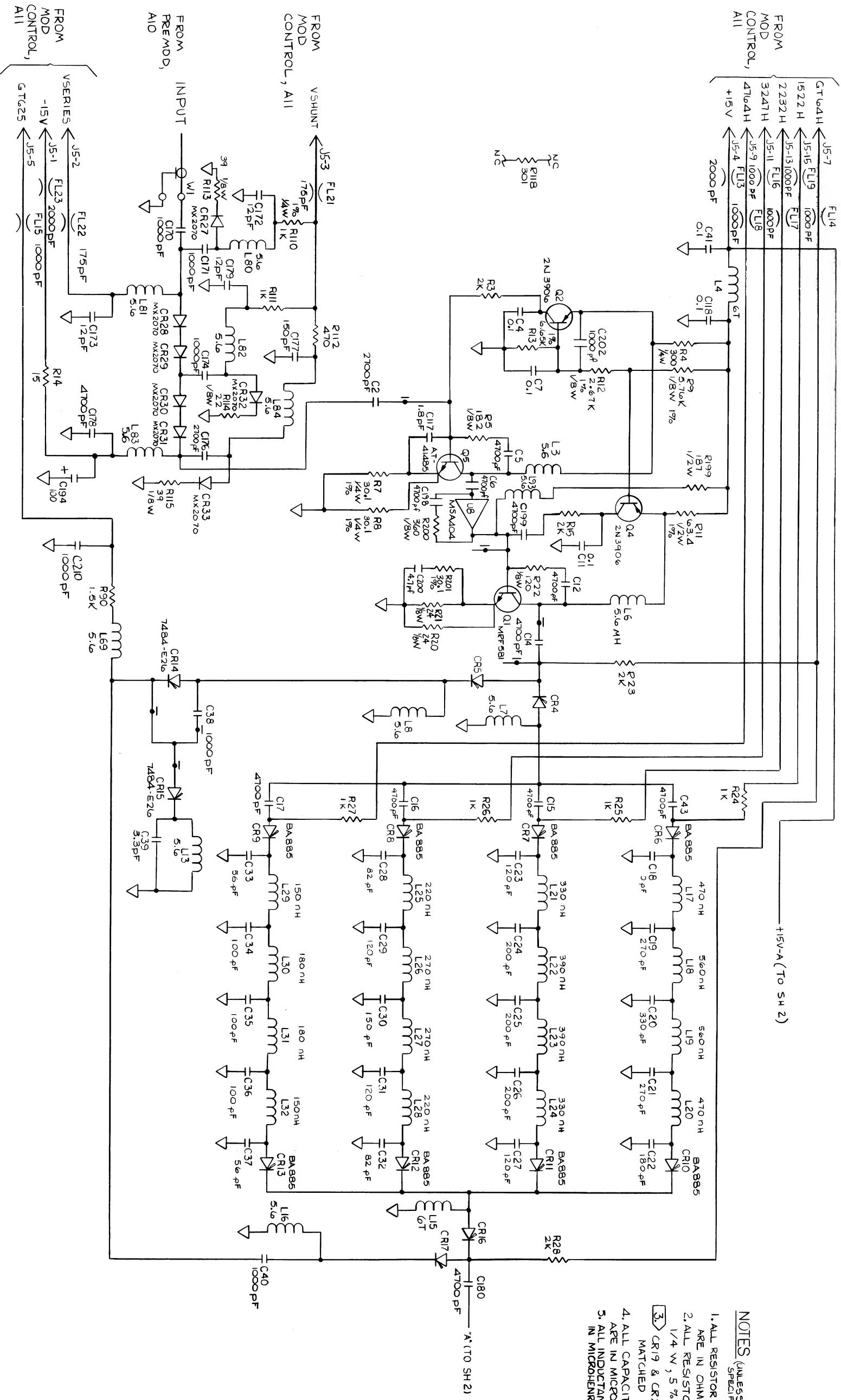


Figure 8-8. A8 Output PCA



SCHEMATIC DIAGRAMS

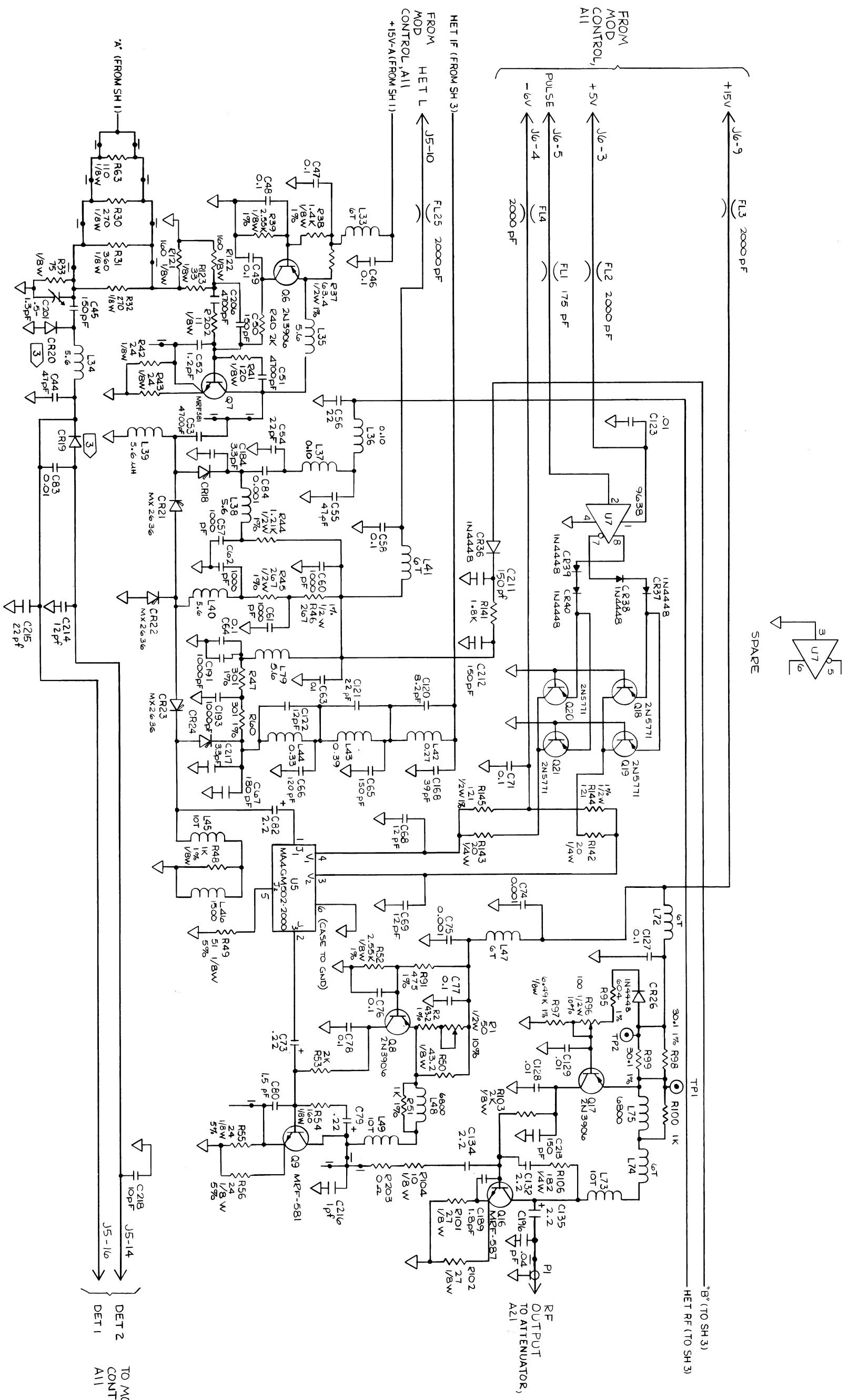


Figure 8-8. A8 Output PCA (cont)

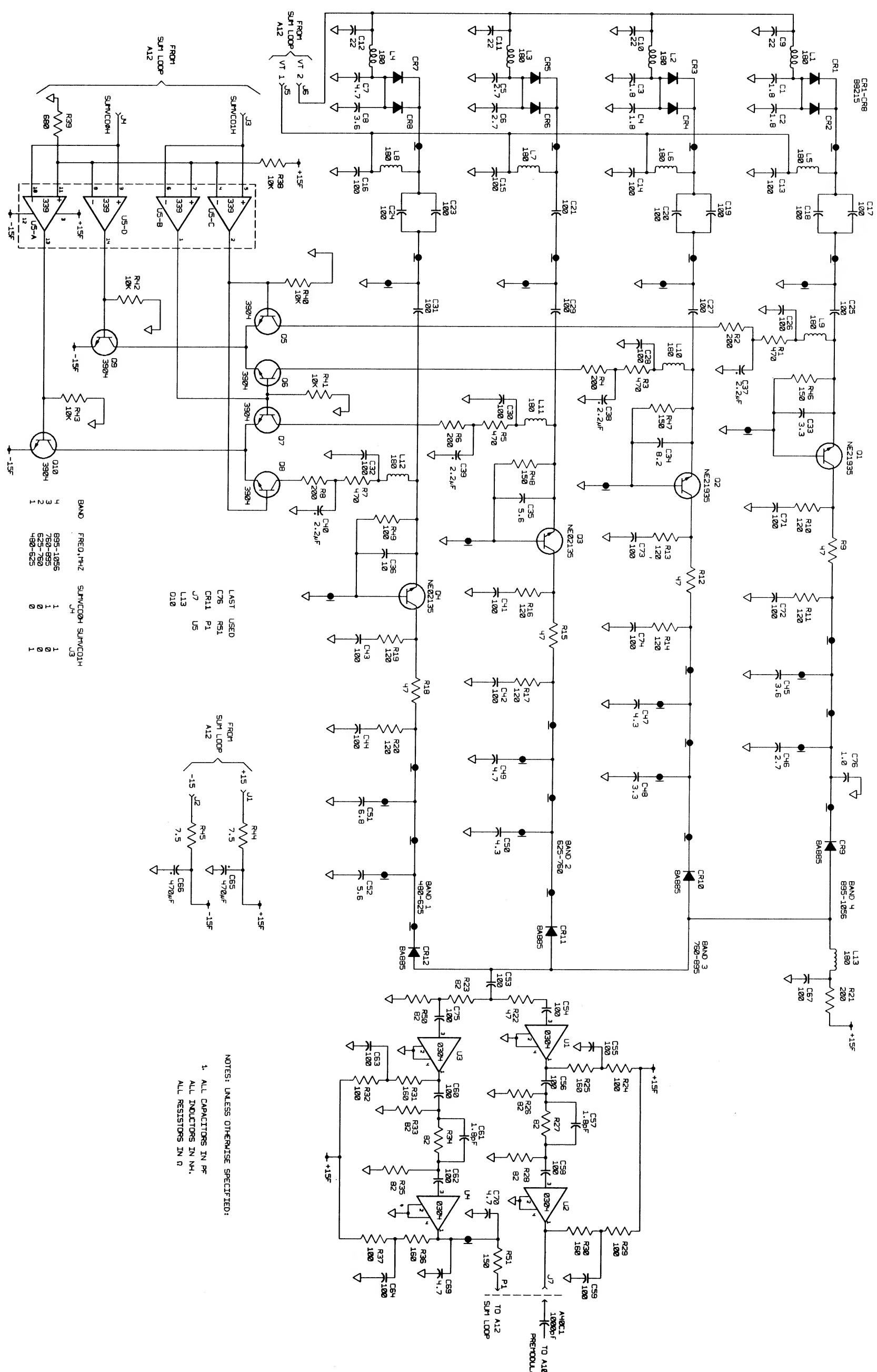
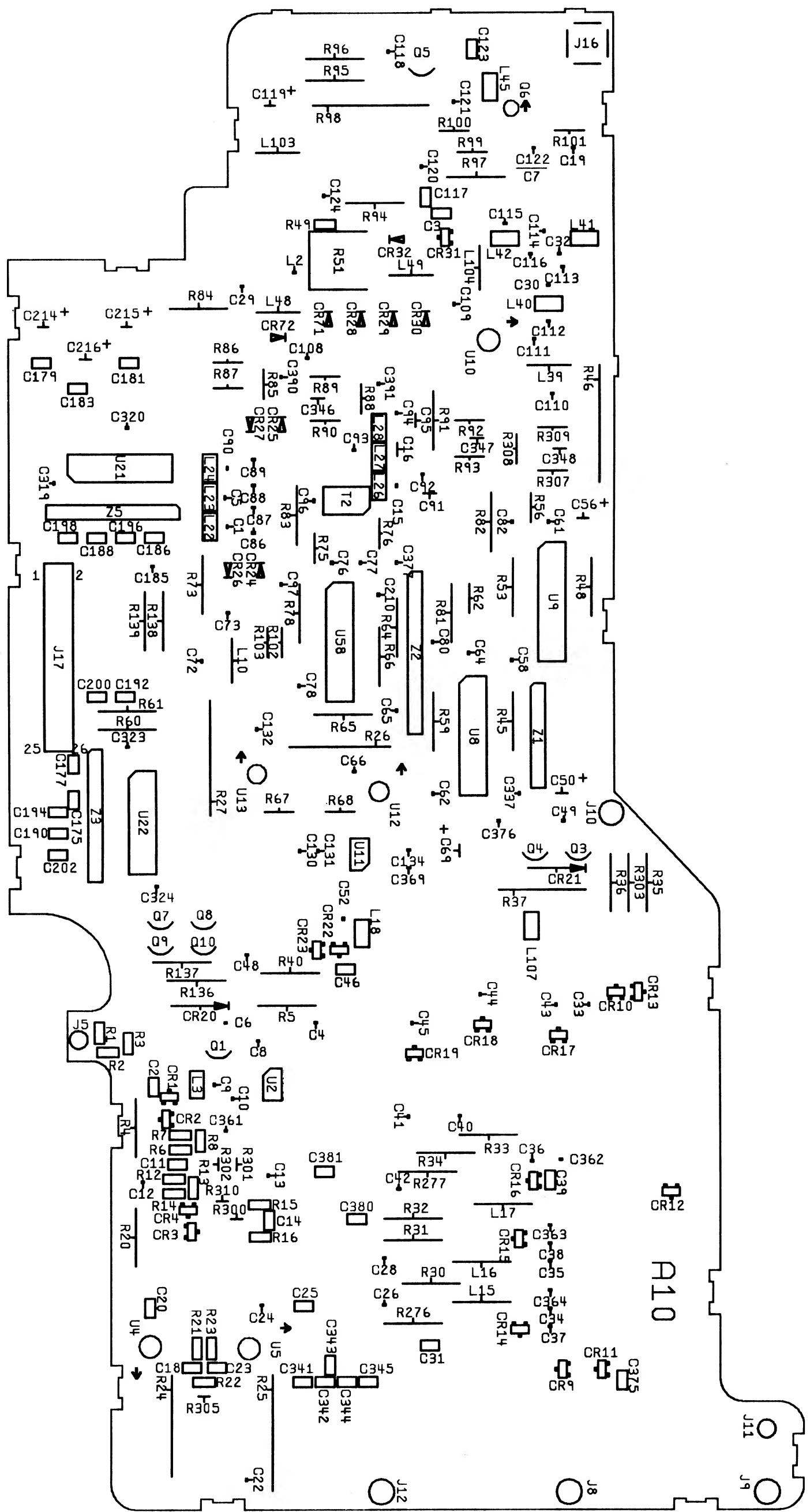


Figure 8-9. A9 Sum Loop VCO PCA (cont)

SCHEMATIC DIAGRAMS



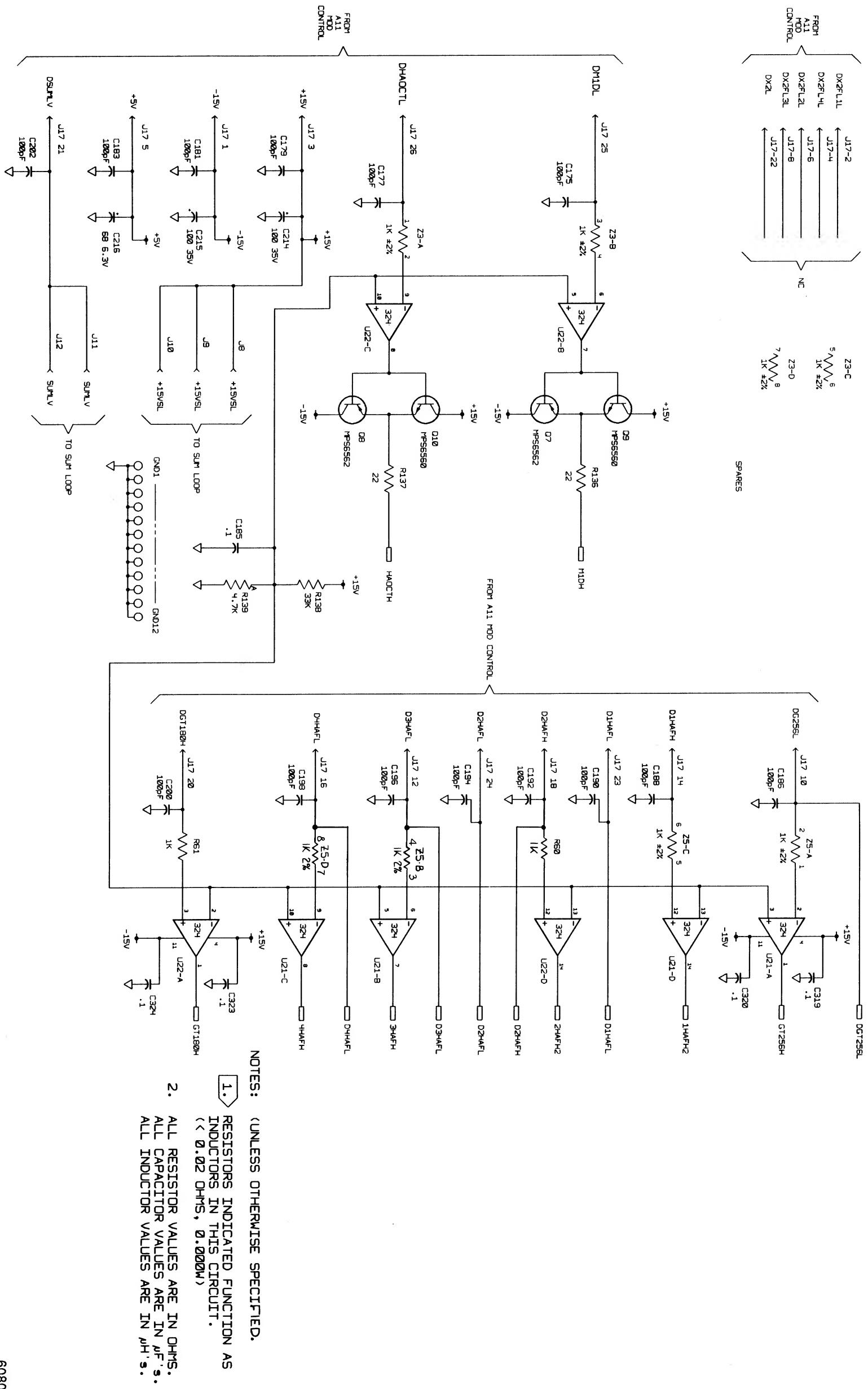


Figure 8-10. A10 Premodulator PCA (cont)

6080A-1046
(1 of 3)

SCHEMATIC DIAGRAMS

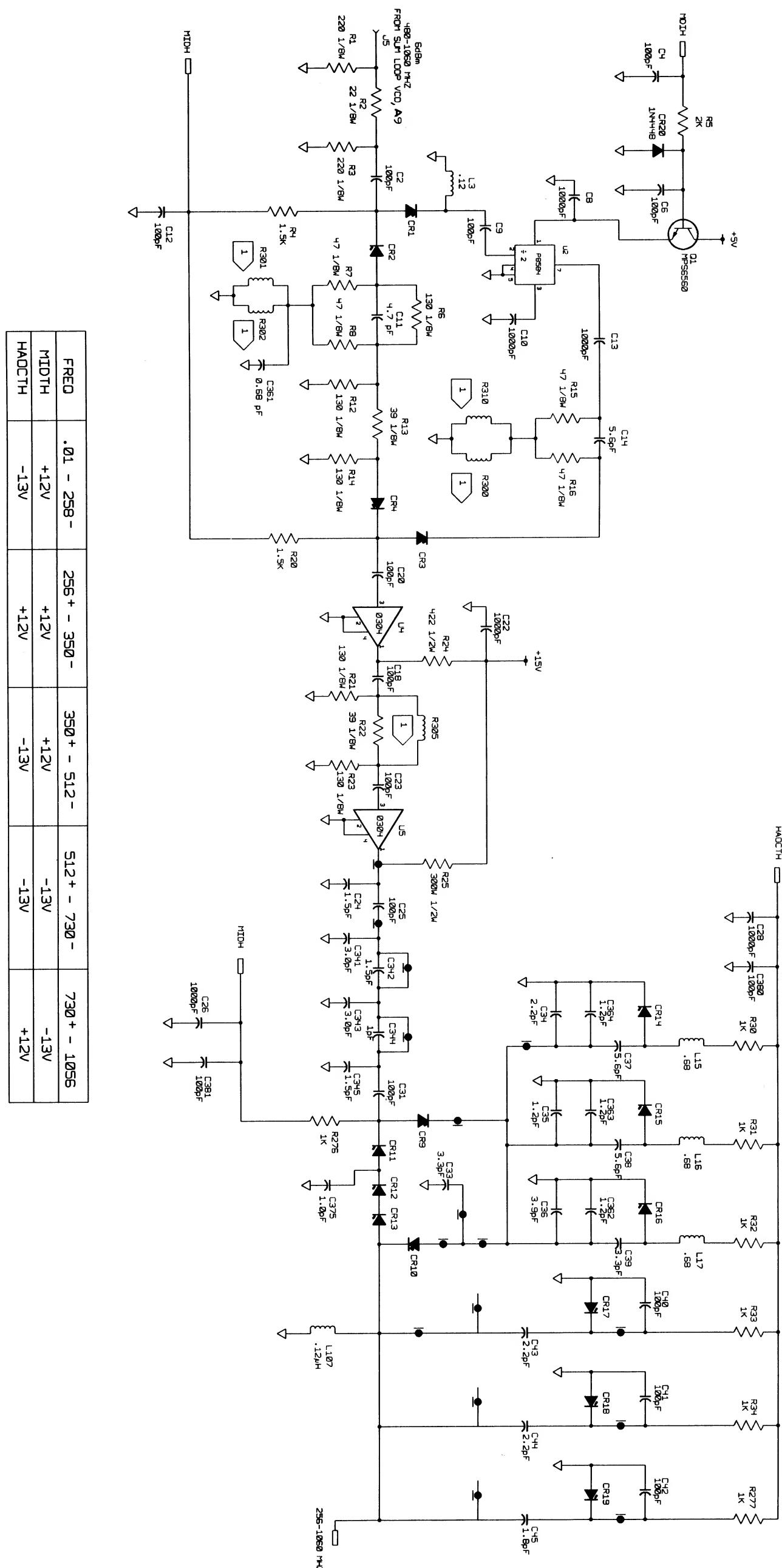


Figure 8-10. A10 Premodulator PCA (cont)

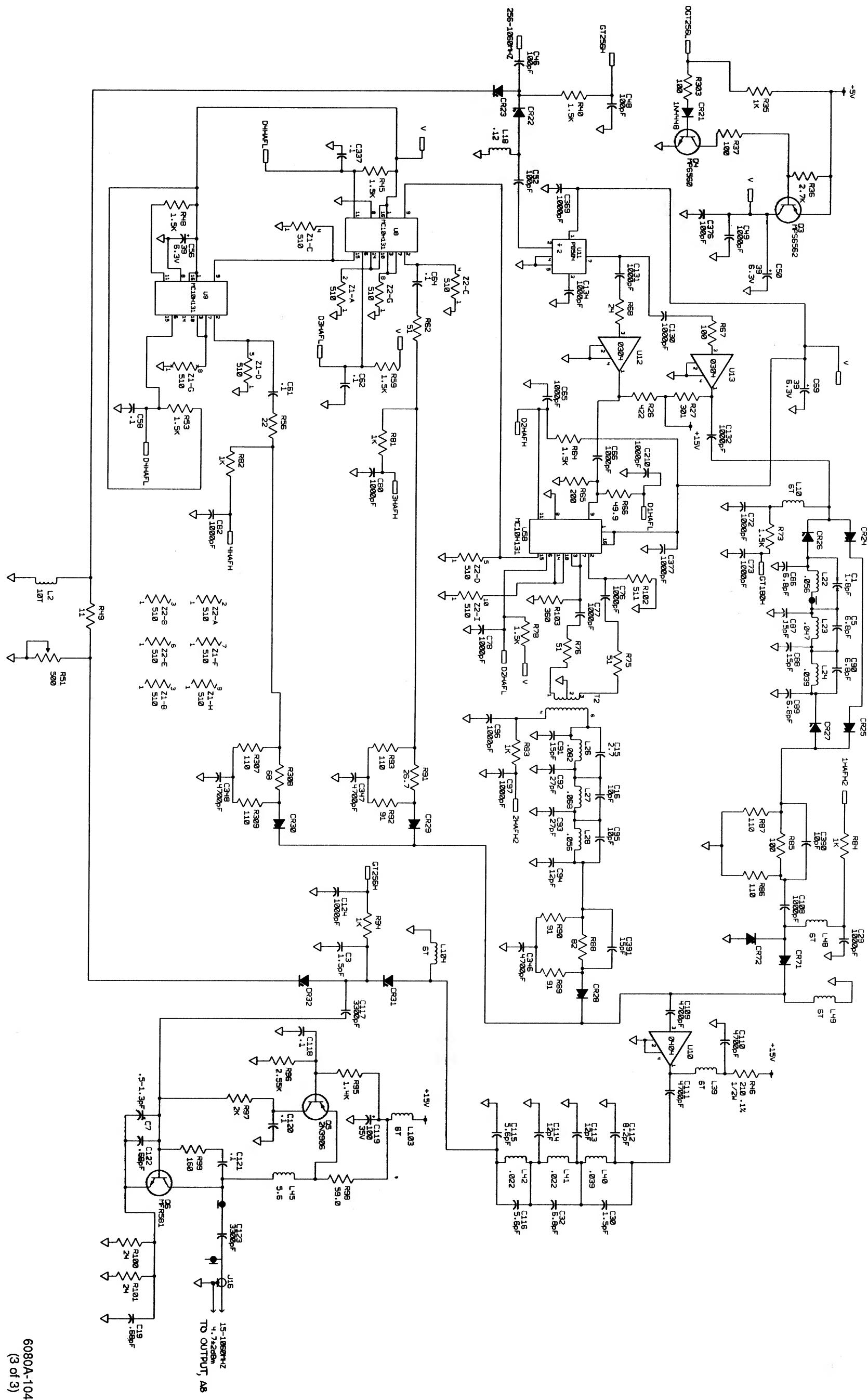


Figure 8-10. A10 Premodulator PCA (cont)

6080A-1046
(3 of 3)

SCHEMATIC DIAGRAMS

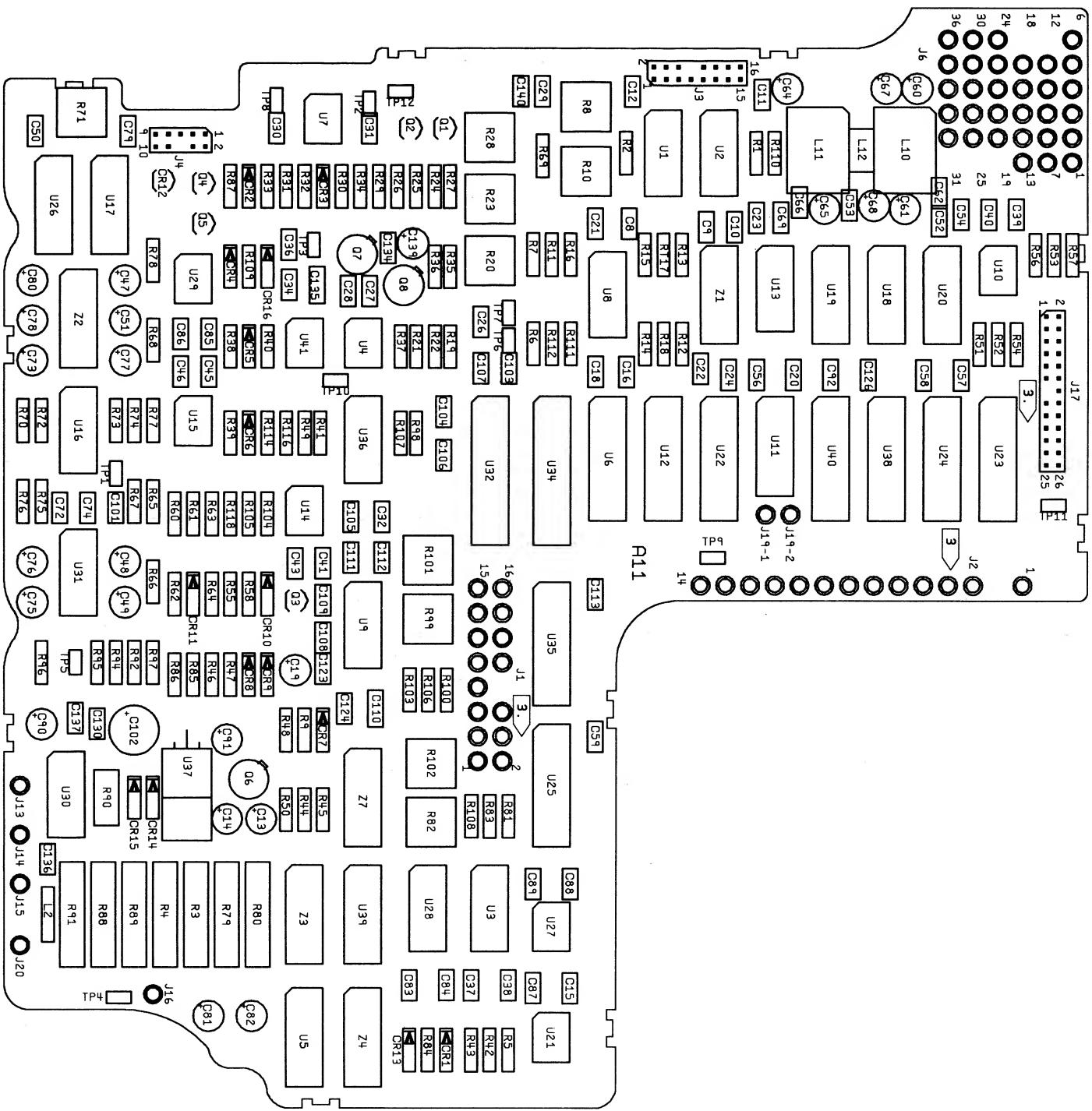


Figure 8-11. A11 Modulation Control PCA

SCHEMATIC DIAGRAMS

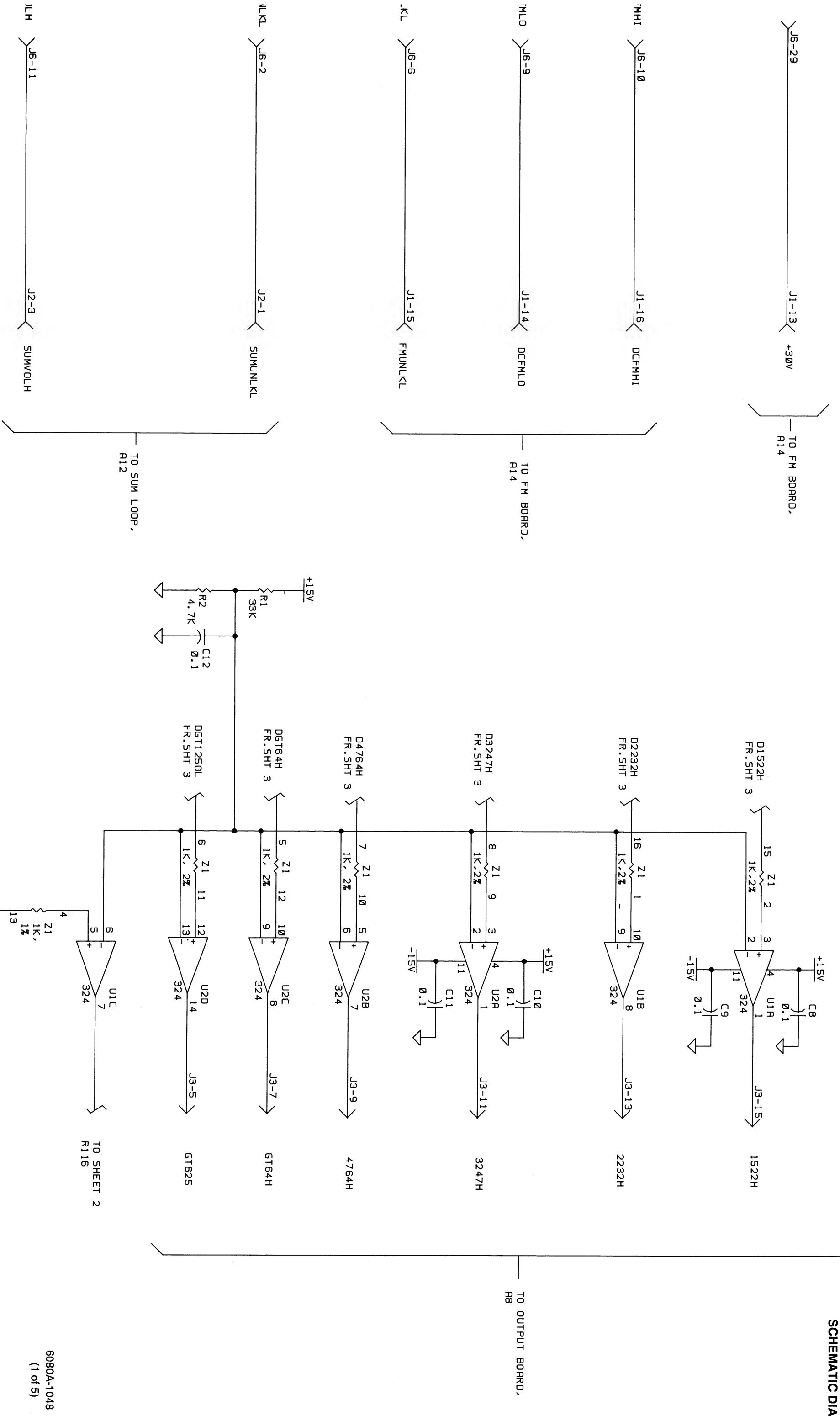
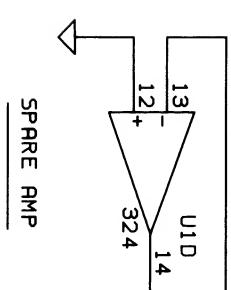
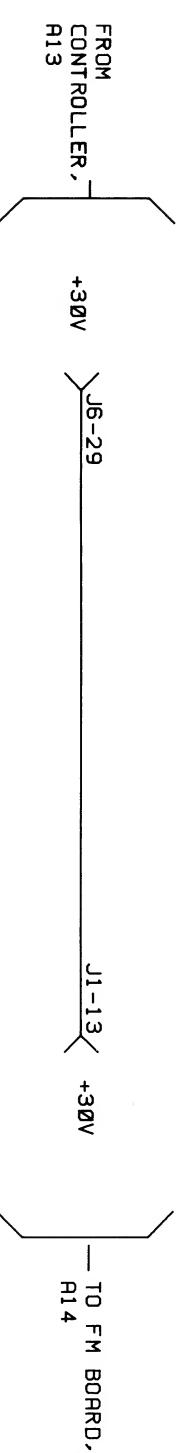
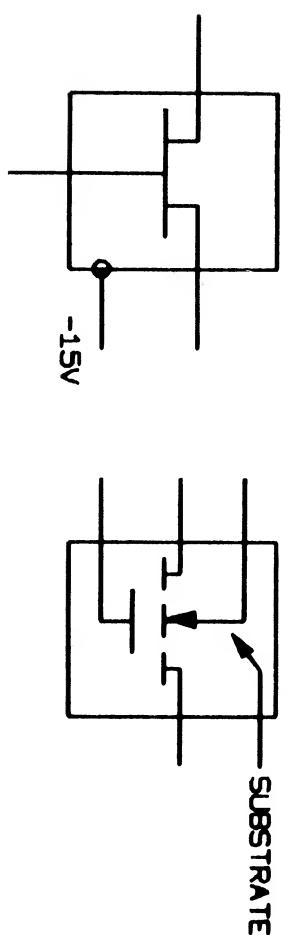


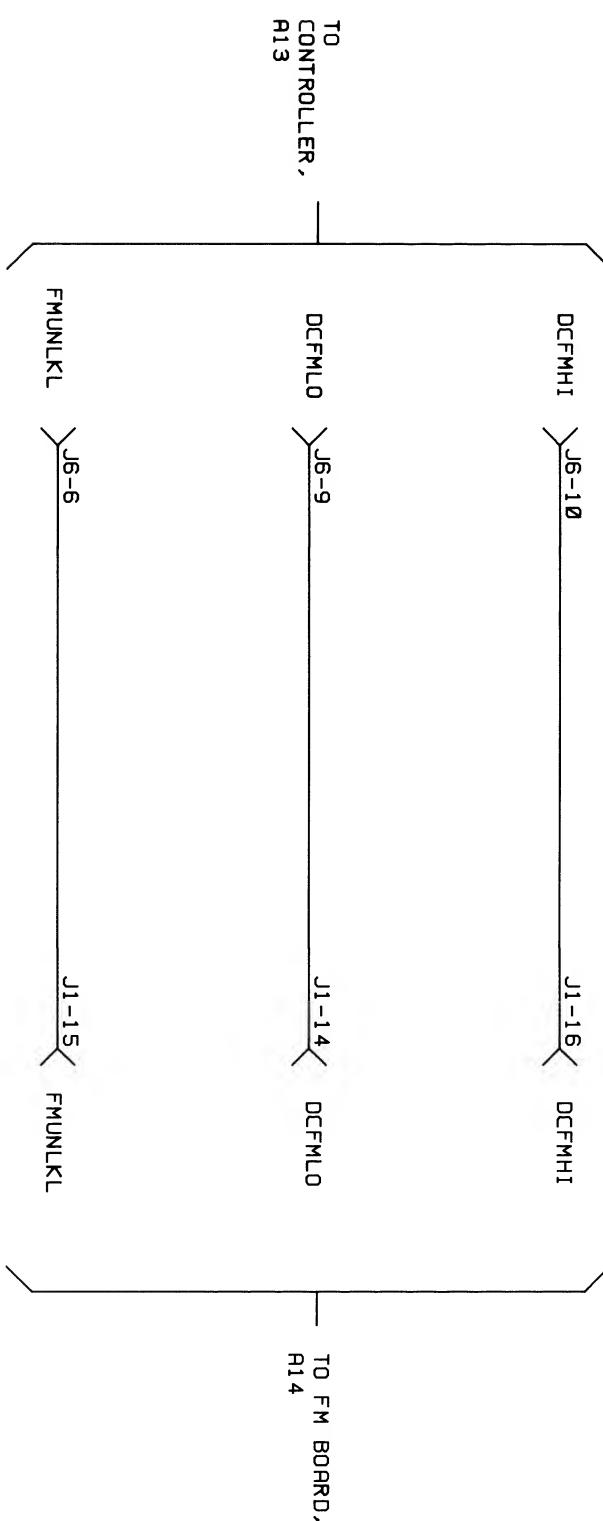
Figure 8-11. A11 Modulation Control PCA (cont)

NOTE:
SD5002'S WHICH
ARE SHOWN AS:

ARE ACTUALLY
CONSTRUCTED:



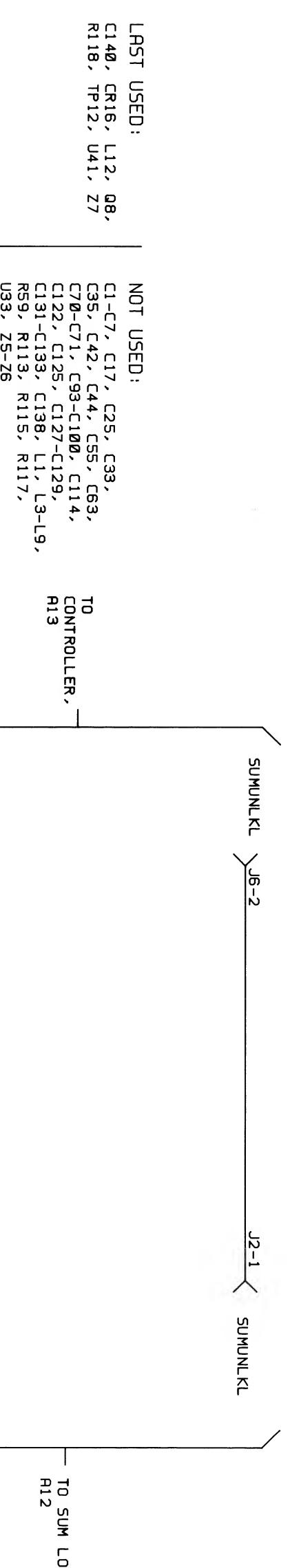
SPARE AMP



SUMNLKL > J6-2

J2-1

SUMNLKL



NOT USED:
C140, CR16, L12, Q8,
R118, TP12, U41, Z7

TO
CONTROLLER,
A13

LAST USED:
C1-C7, C17, C25, C33,
C35, C42, C44, C55, C63,
C70-C71, C93-C100, C114,
C122, C125, C127-C129,
C131-C133, C138, L1, L3-L9,
R59, R113, R115, R117,
U33, Z5-Z6

SCHEMATIC DIAGRAMS

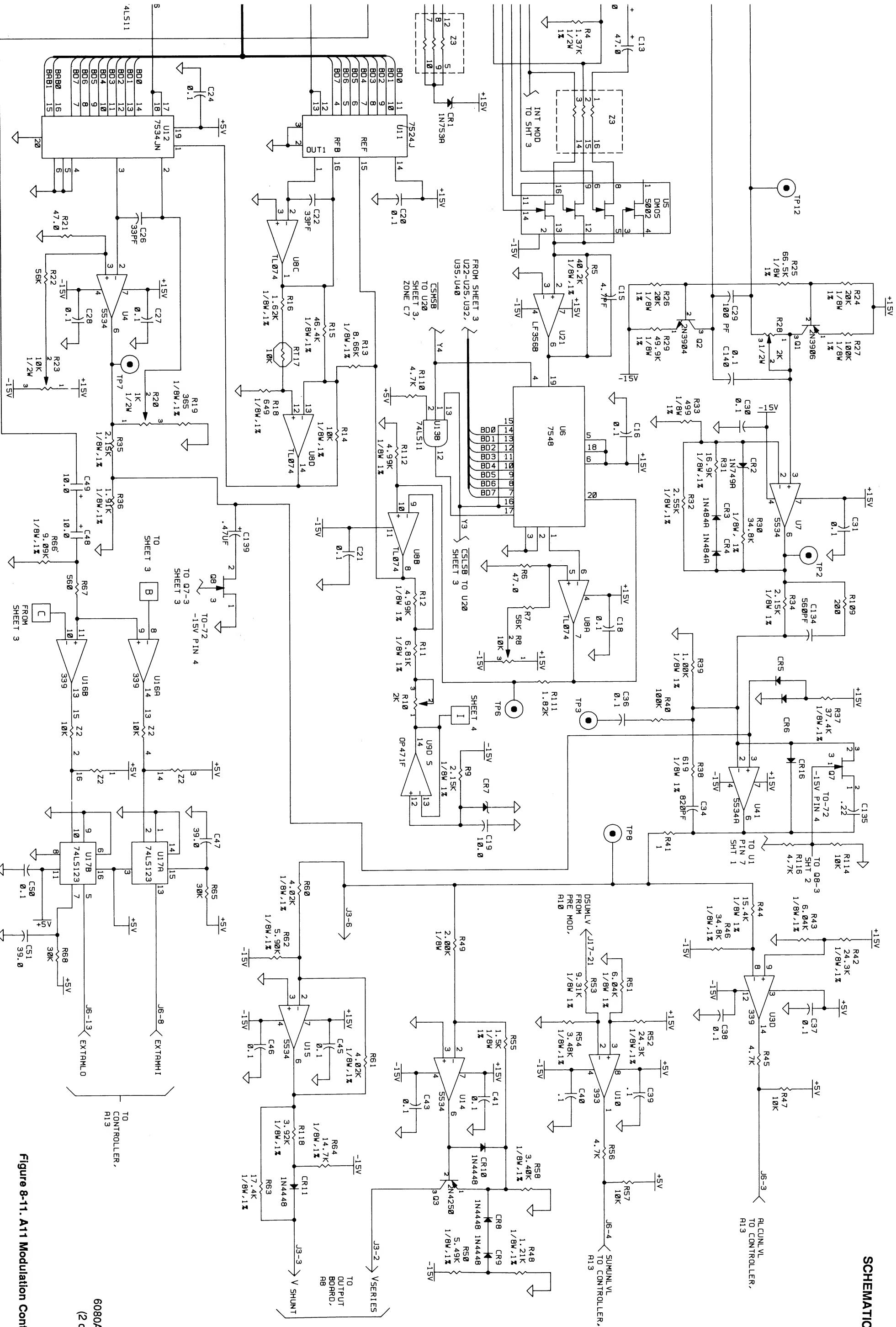
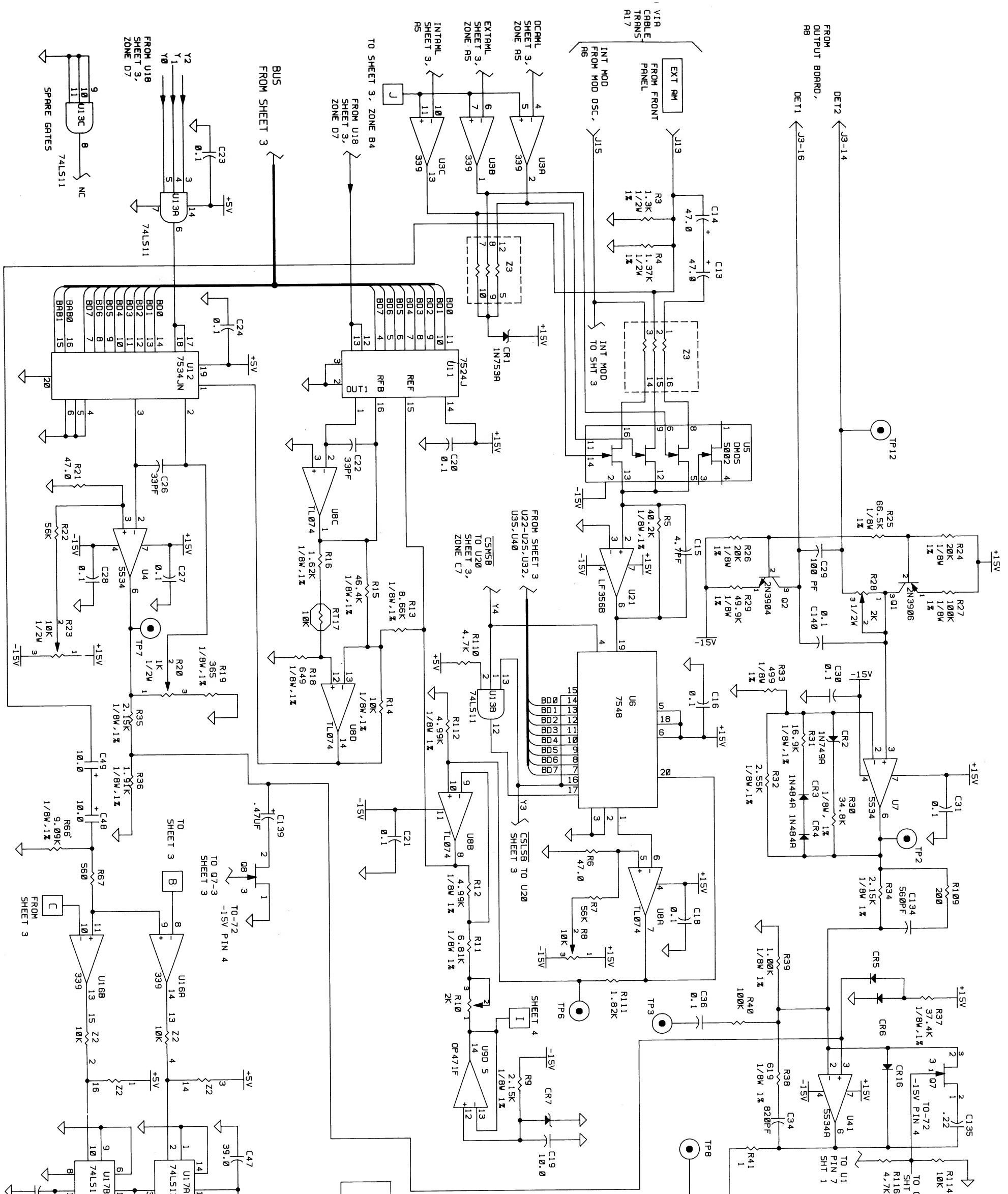
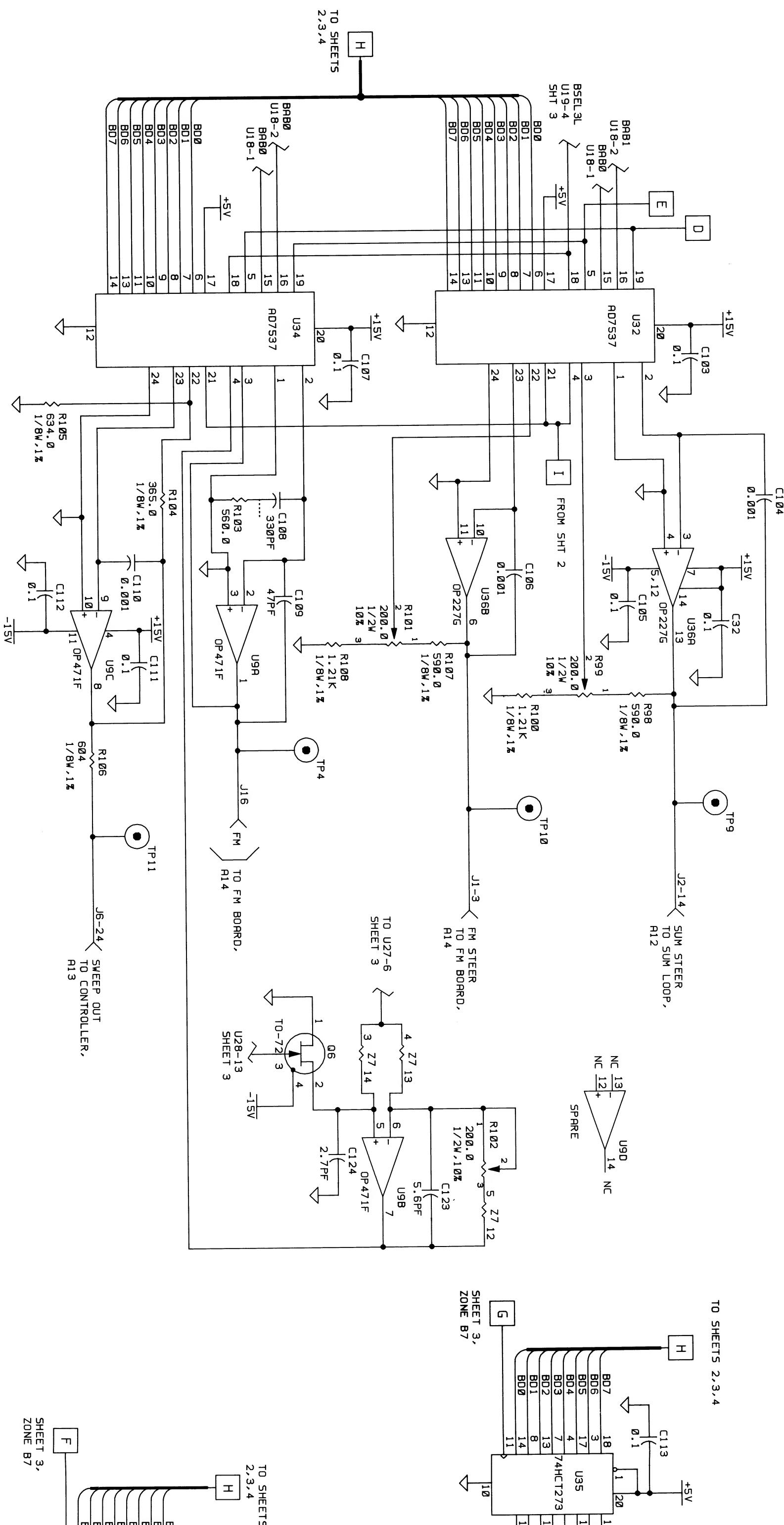
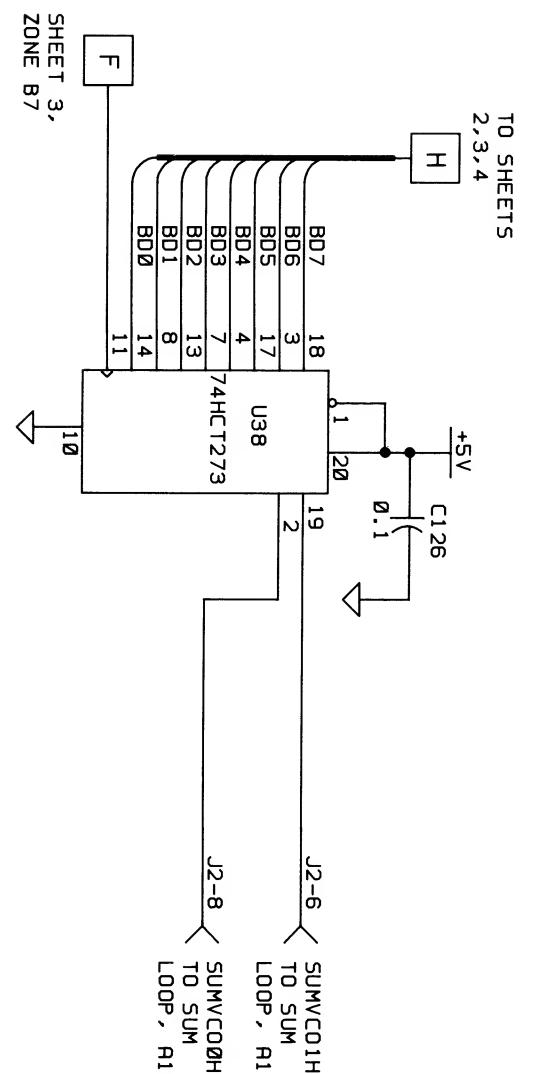
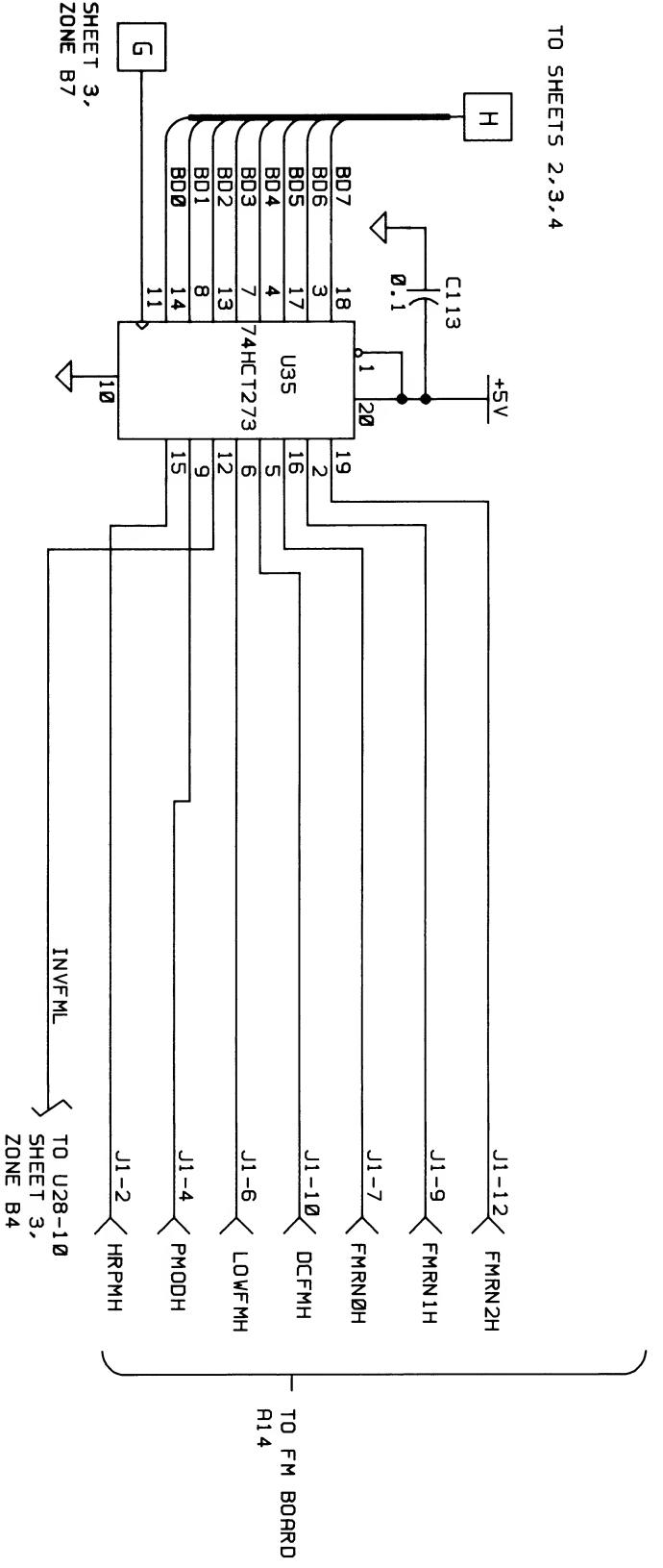
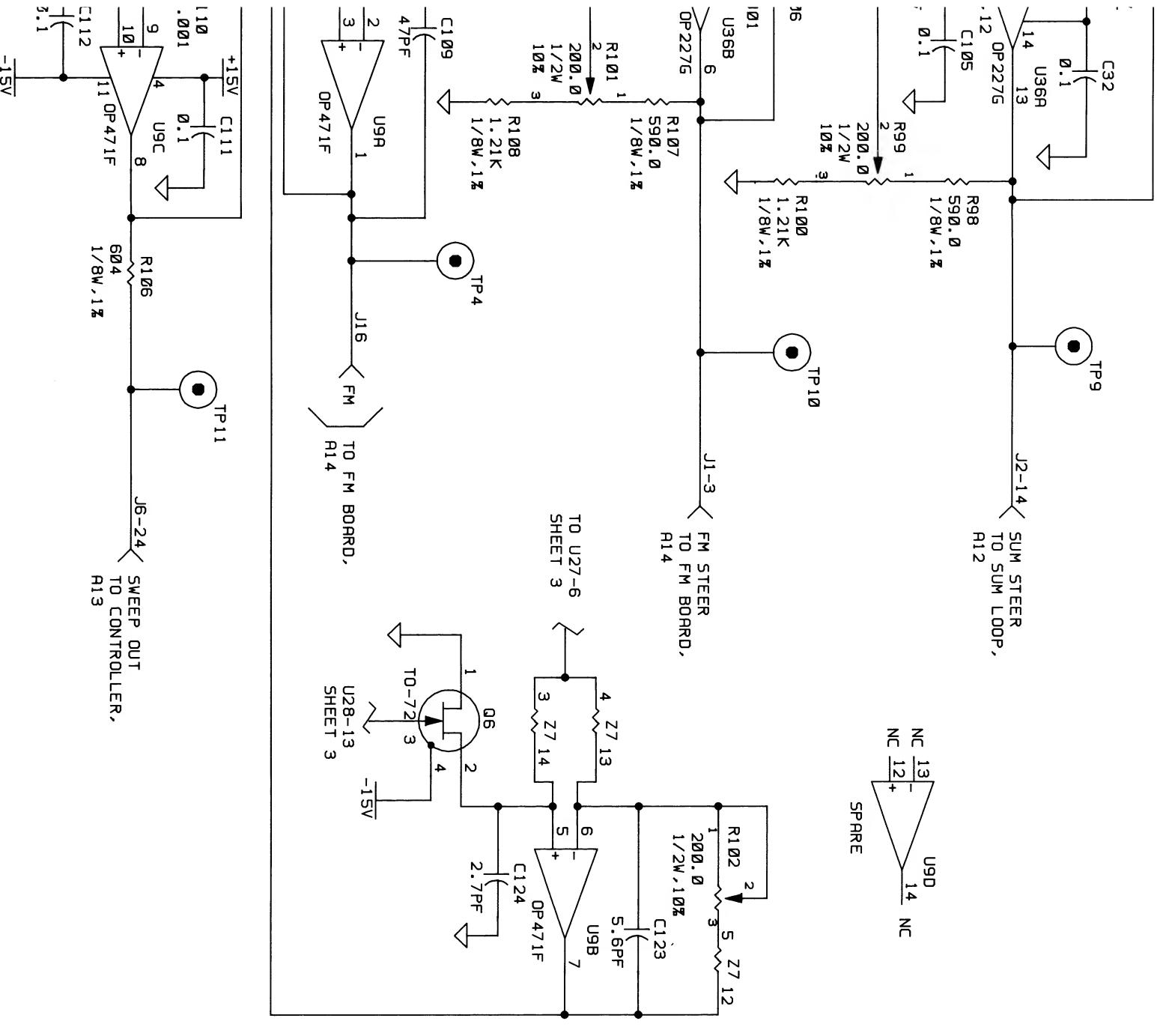


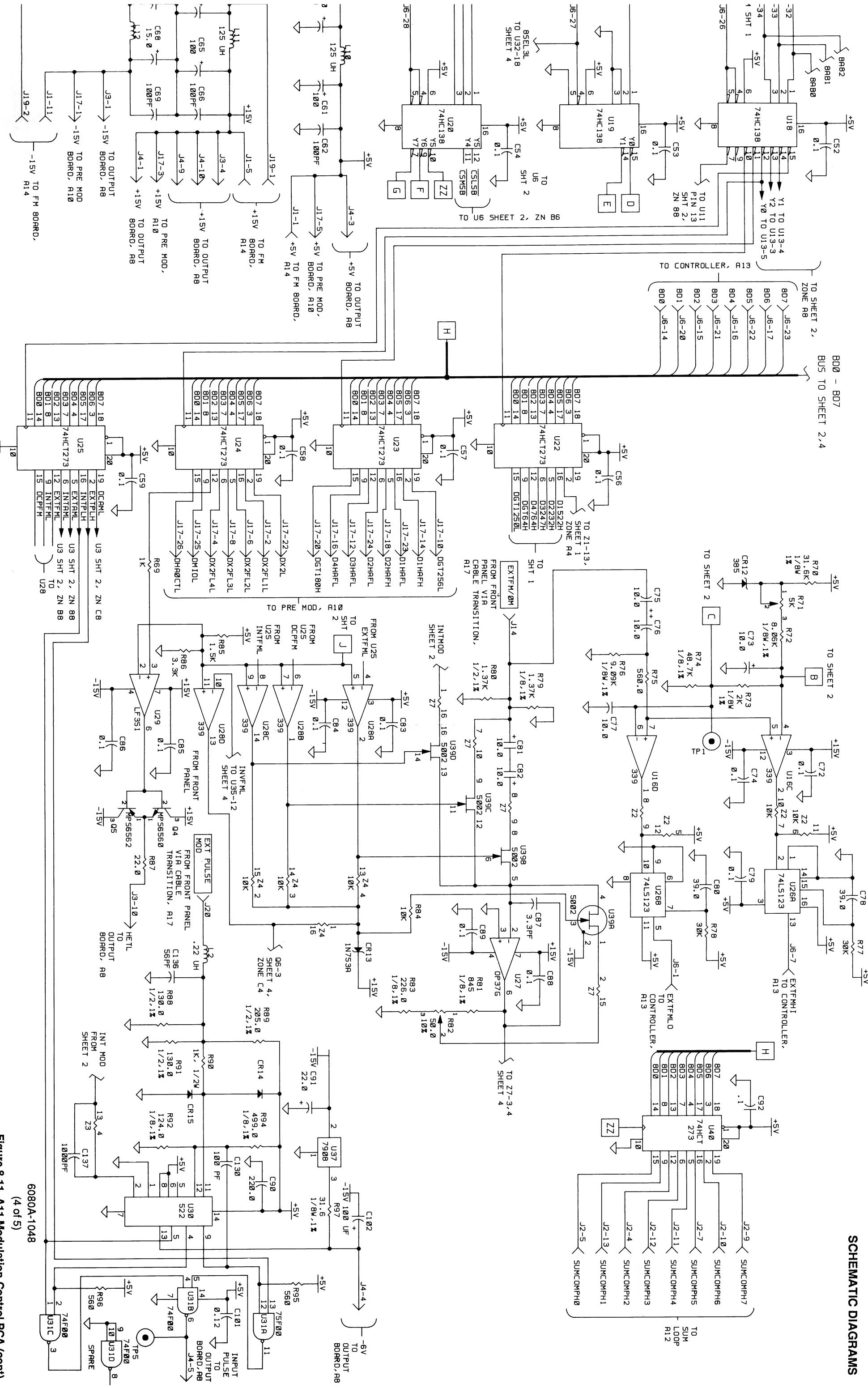
Figure 8-11. A11 Modulation Control PCA (cont)

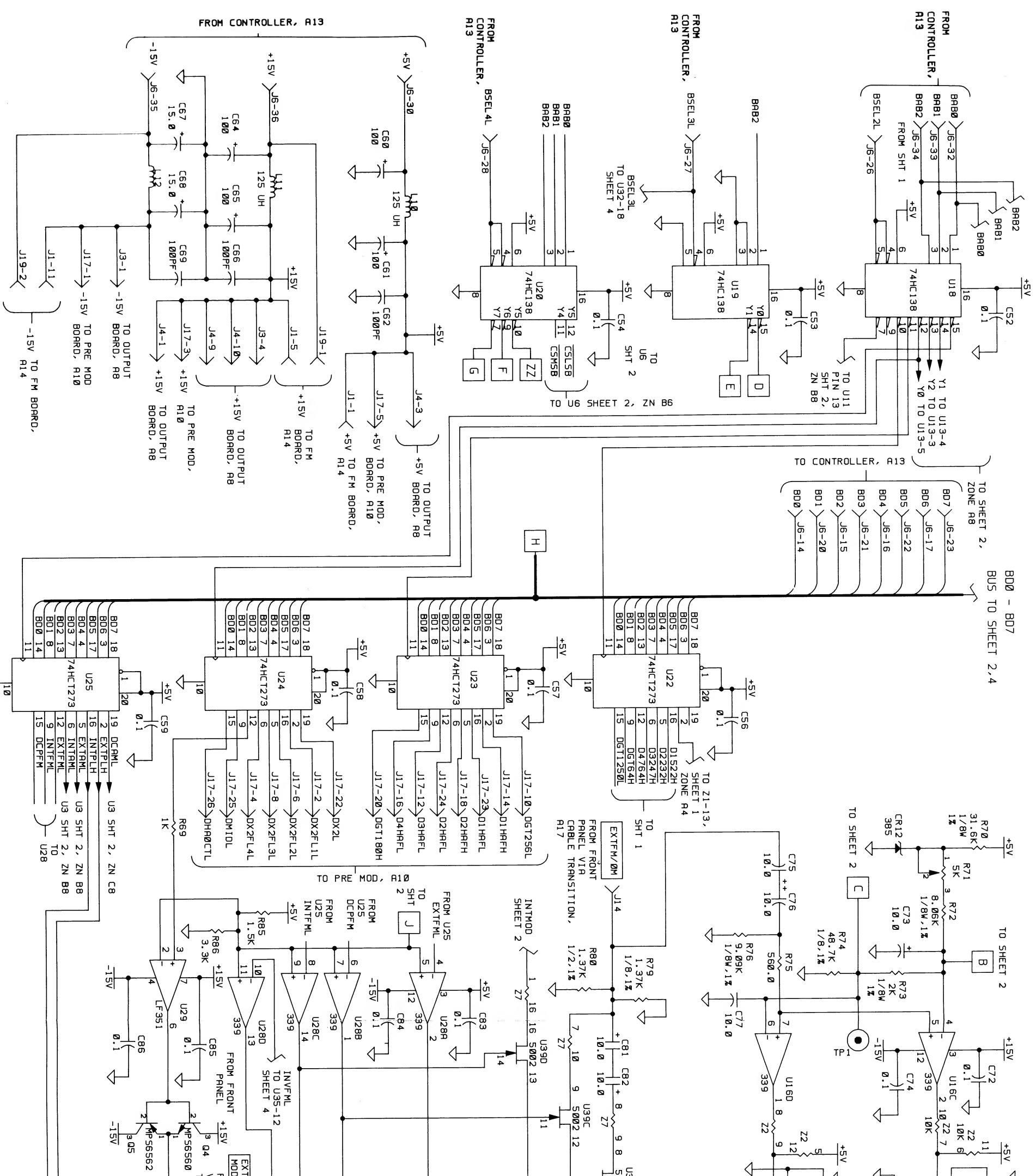






SCHEMATIC DIAGRAMS





CONNECTOR SUMMARY

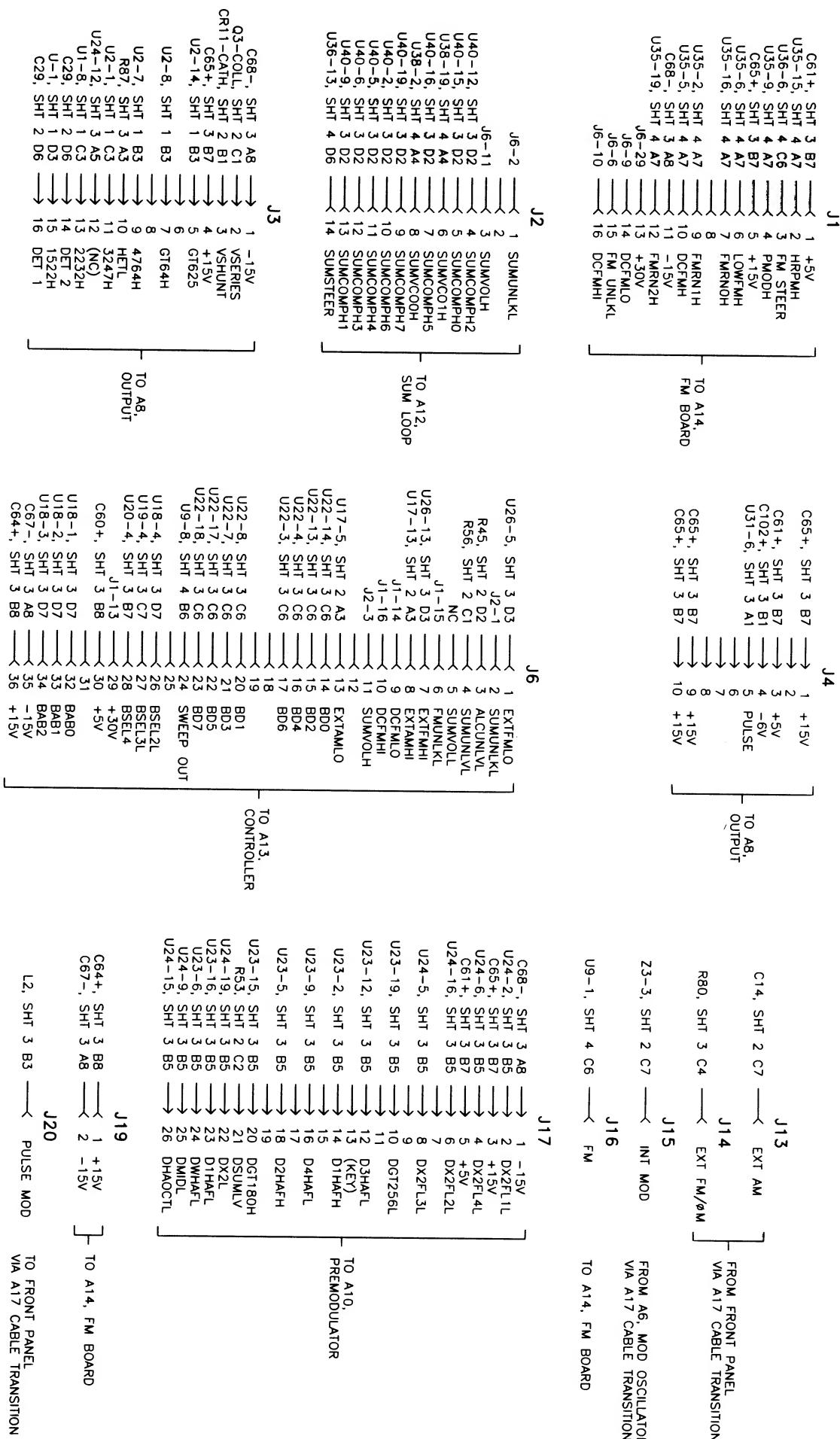
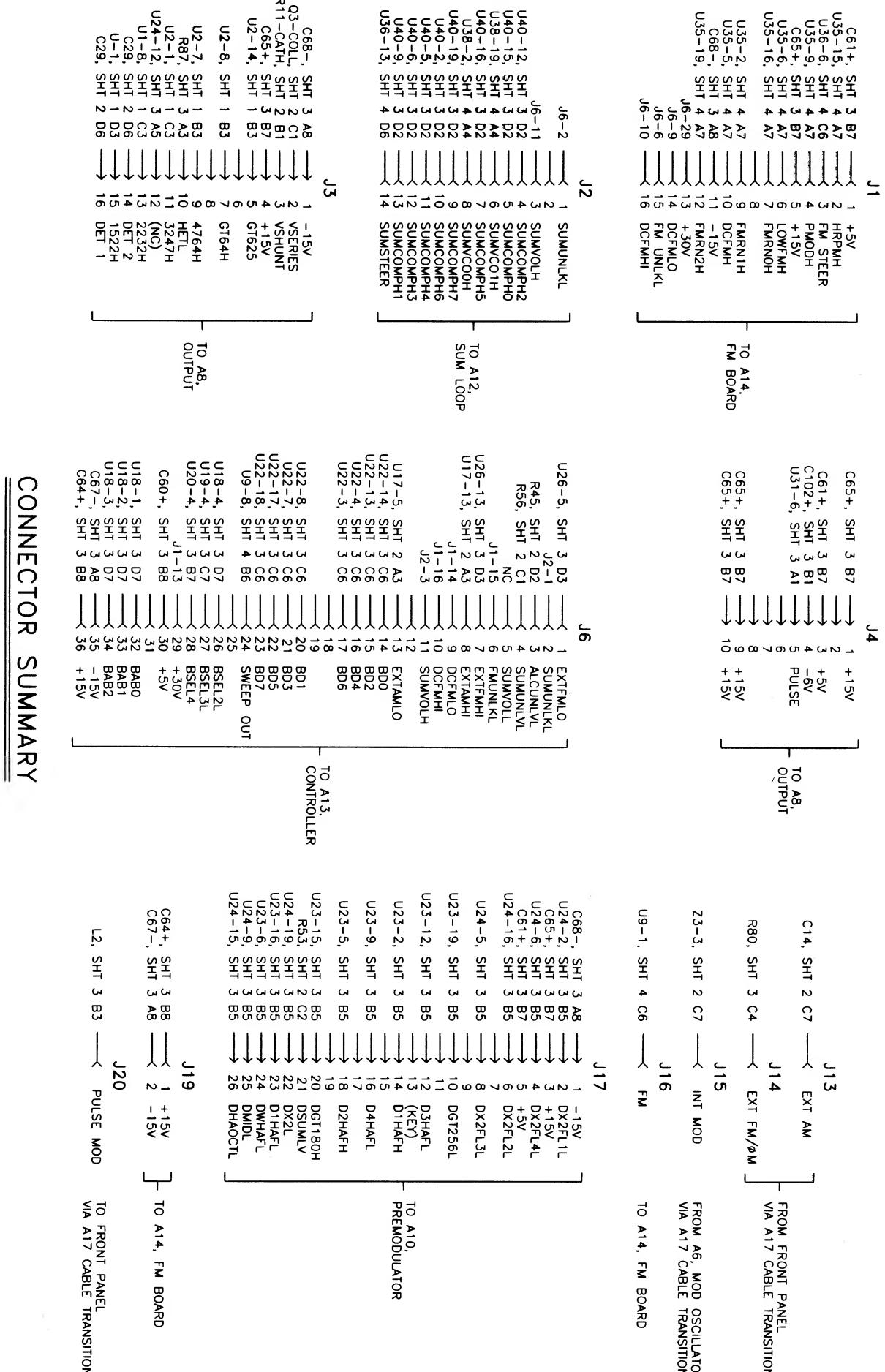


Figure 8-11. A11 Modulation Control PCA (cont)



SCHEMATIC DIAGRAMS

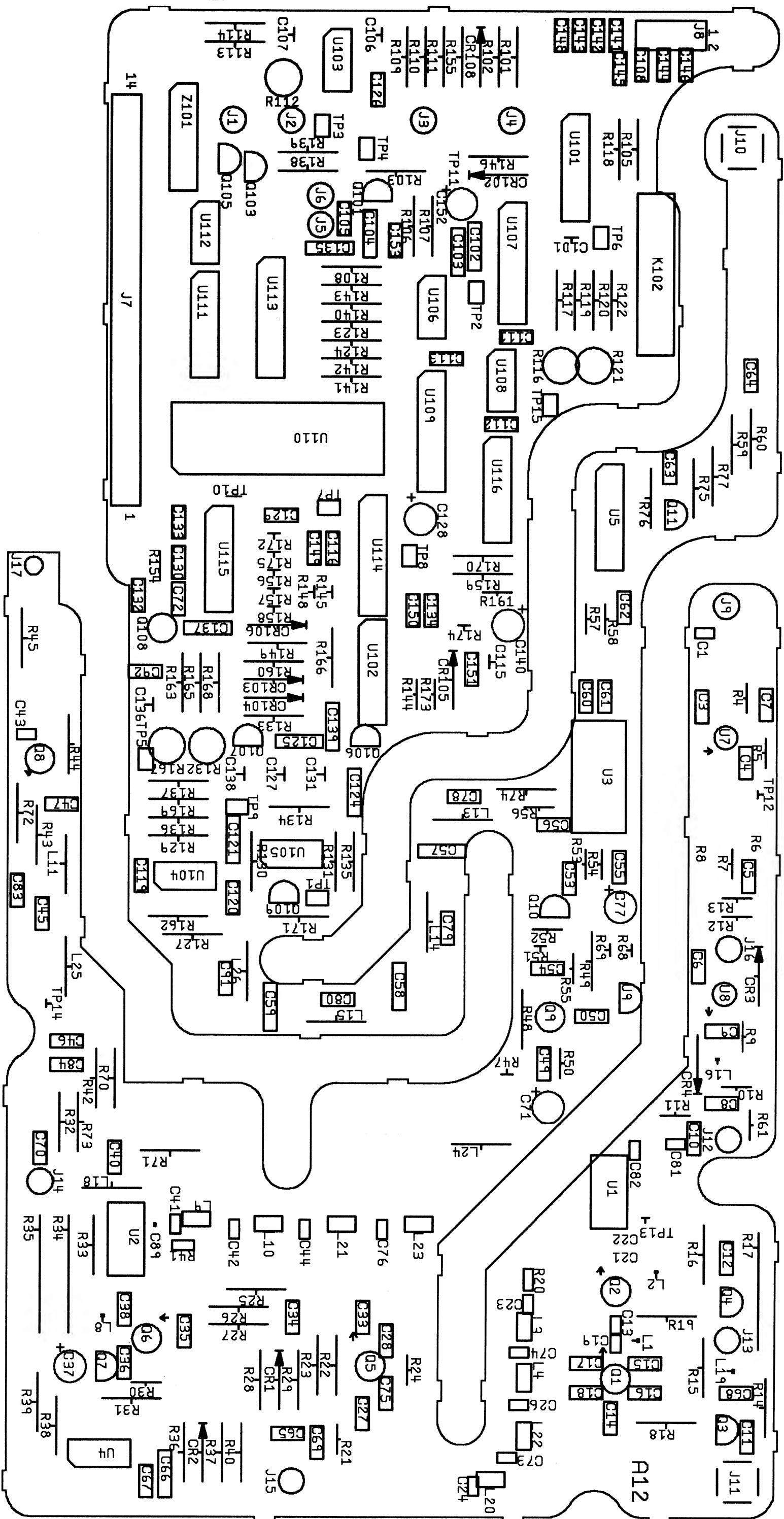


Figure 8-12. A12 Sum Loop PCA

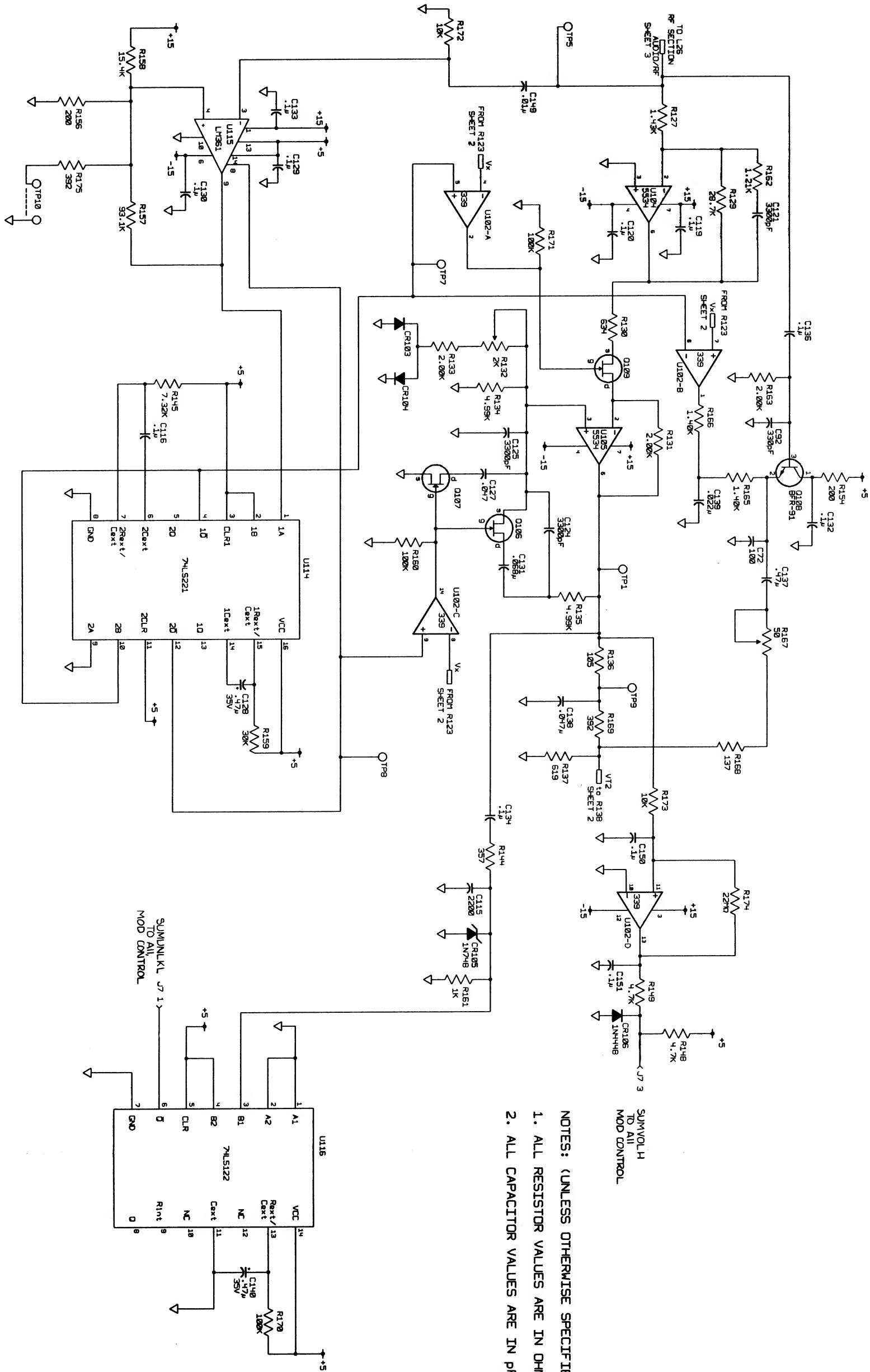


Figure 8-12. A12 Sum Loop PCA (cont)

6080A-1042
(1 of 3)

Schematic Diagrams

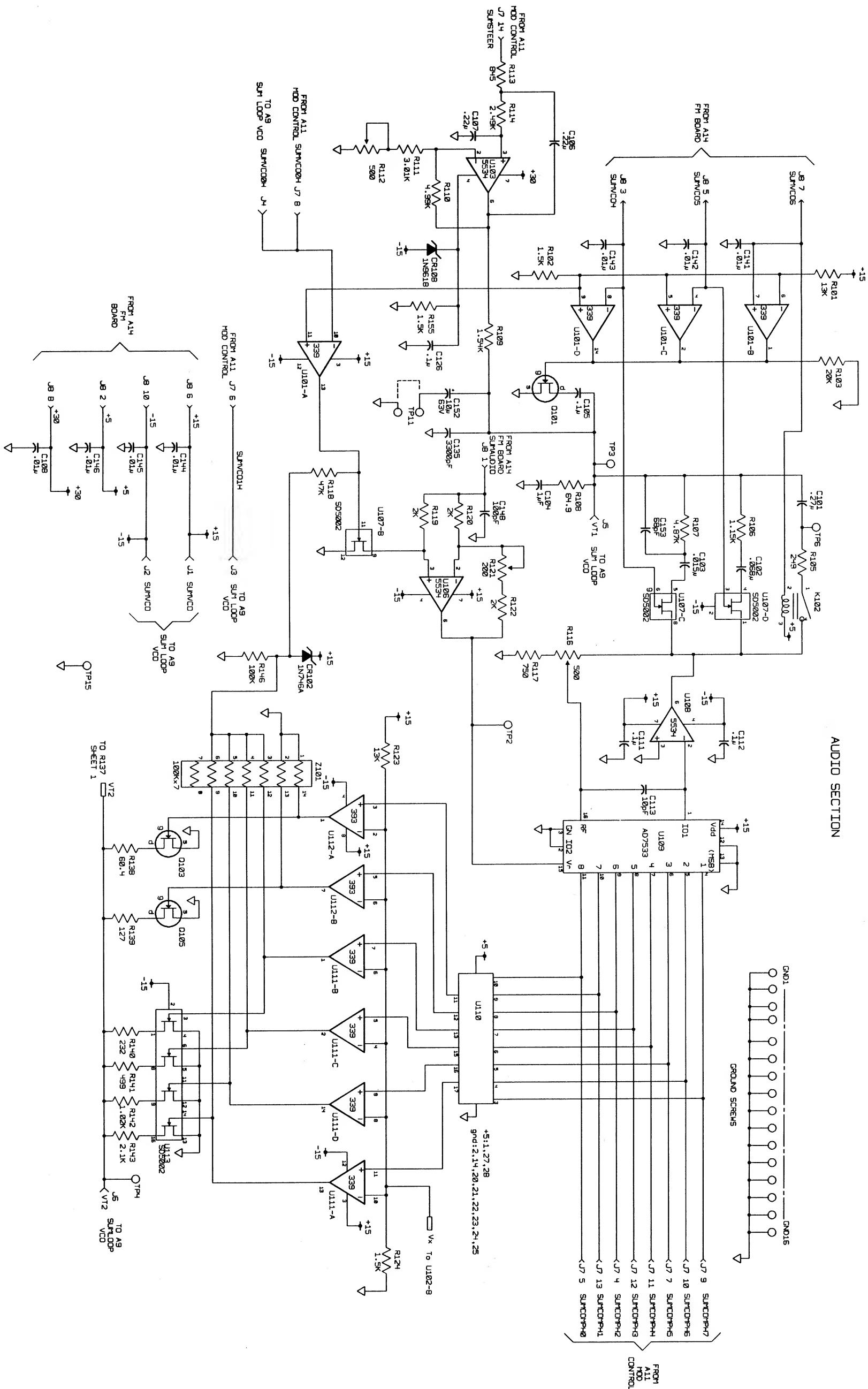


Figure 8-12. A12 Sum Loop PCA (cont)

주 SECTION

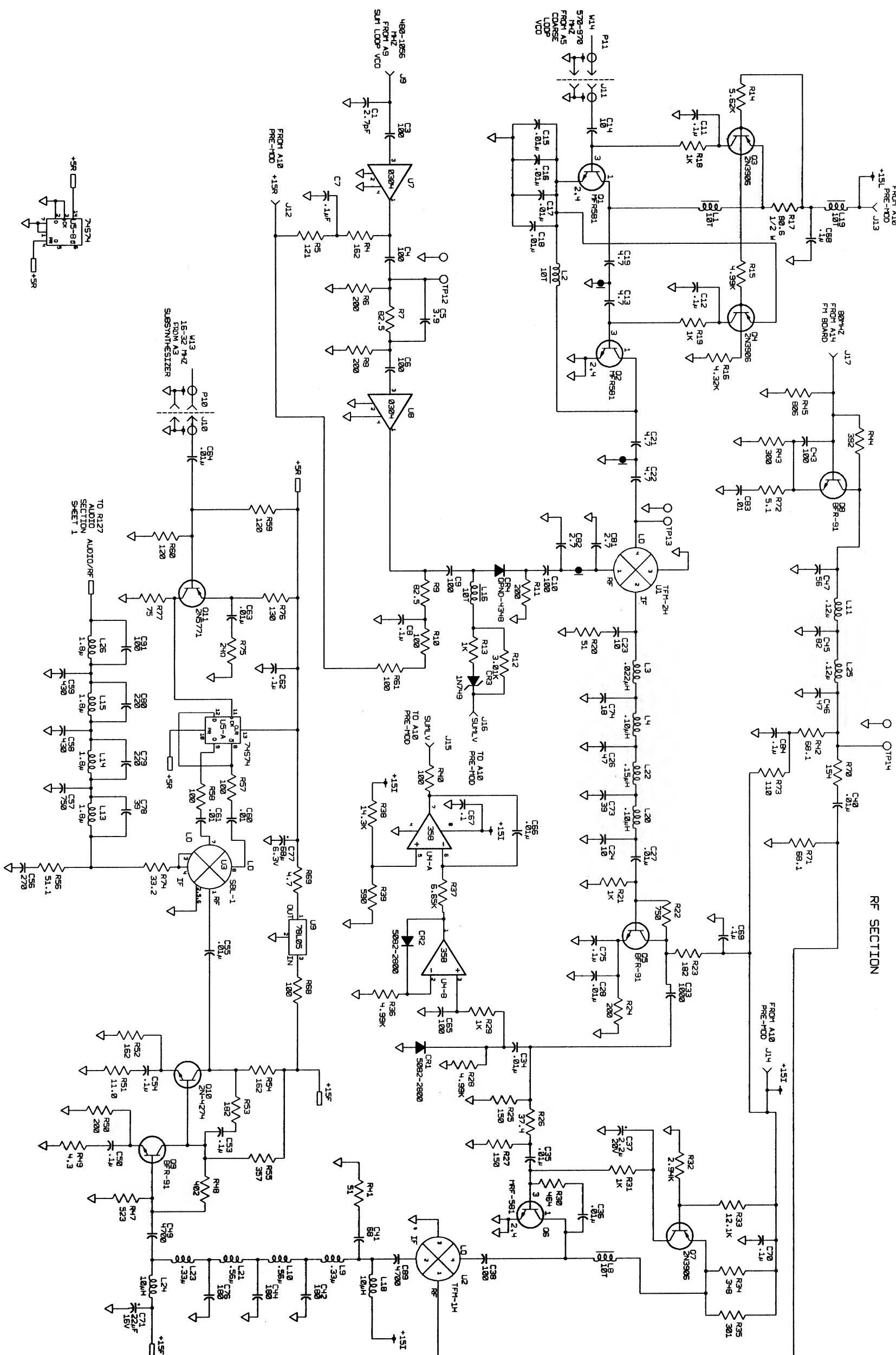


Figure 8-12. A12 Sum Loop PCA (cont)

6080A-104;
(3 of 3)

SCHEMATIC DIAGRAMS

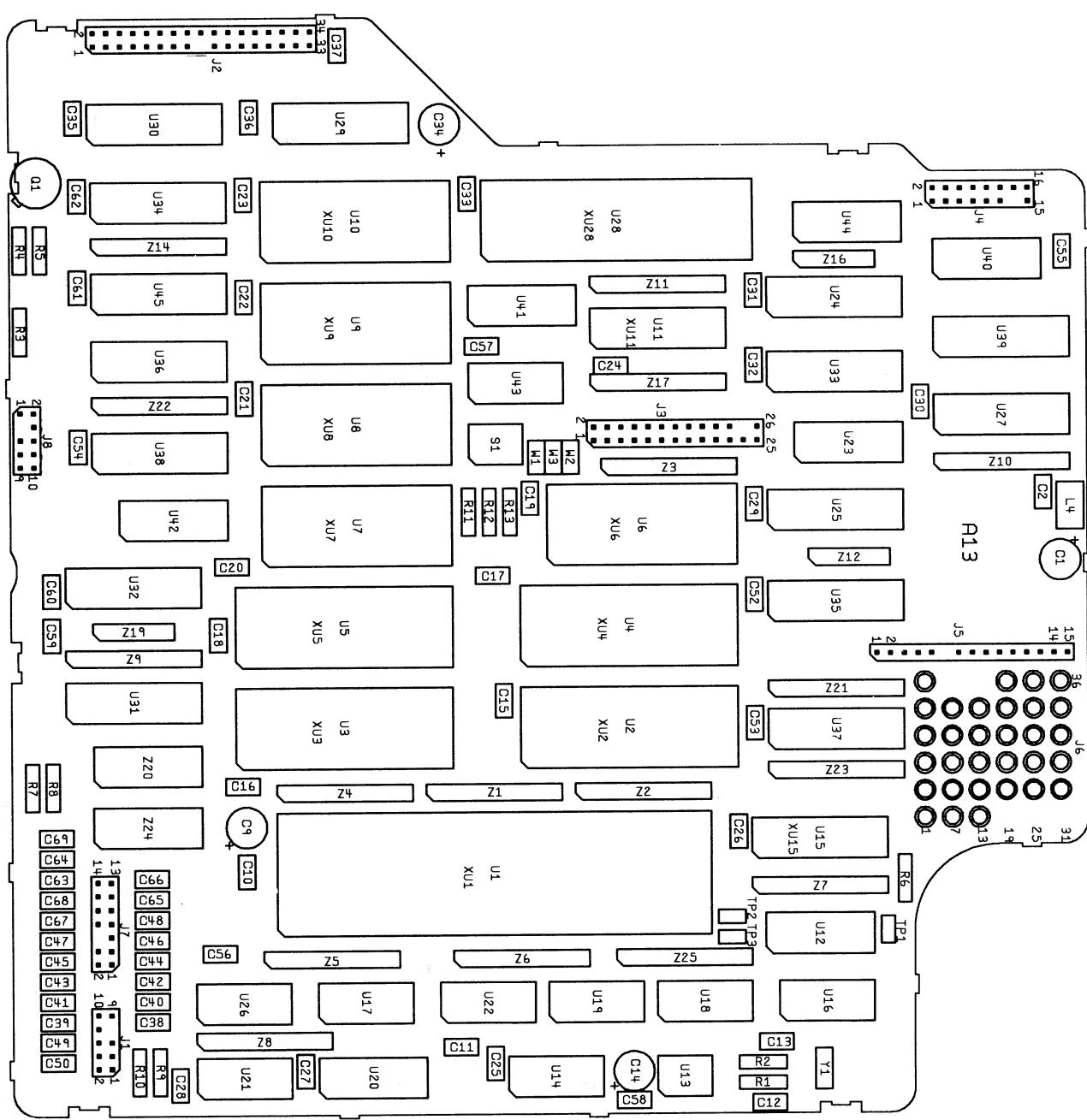
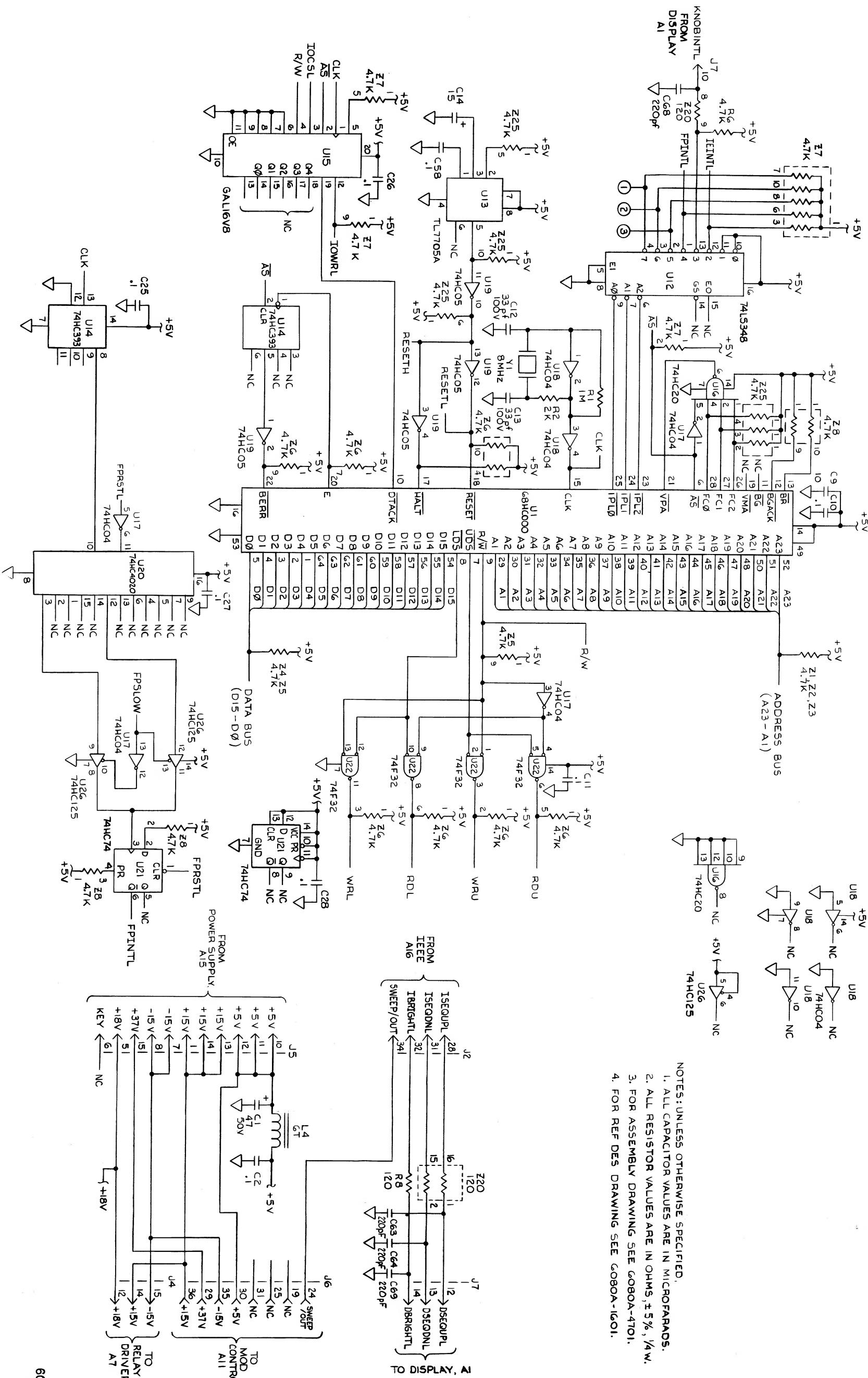


Figure 8-13. A13 Controller PCA

Schematic Diagrams

Figure 8-13. A13 Controller PCA (cont)

6080A-1043
(1 of 4)



SCHEMATIC DIAGRAMS

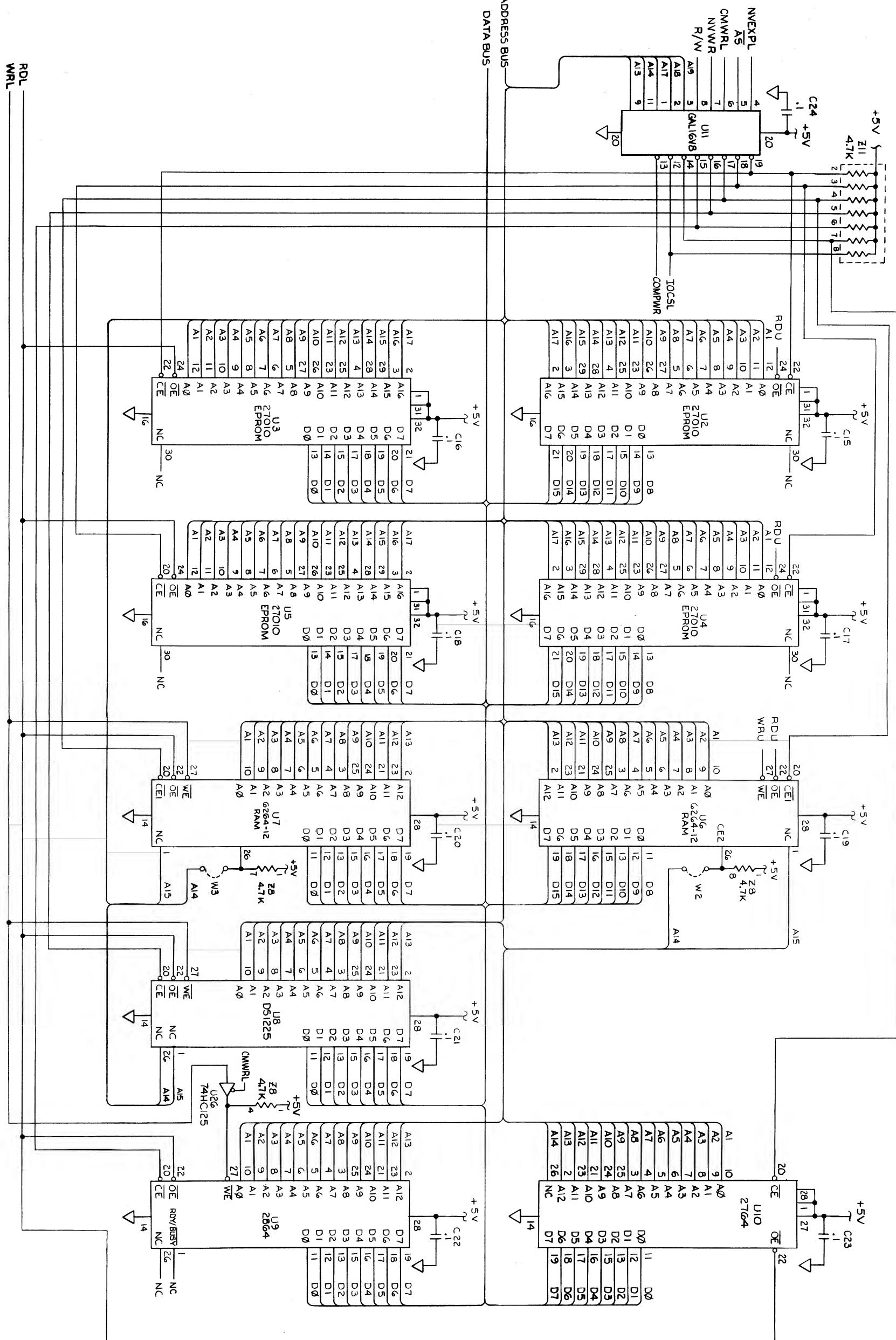


Figure 8-13. A13 Controller PCA (cont)

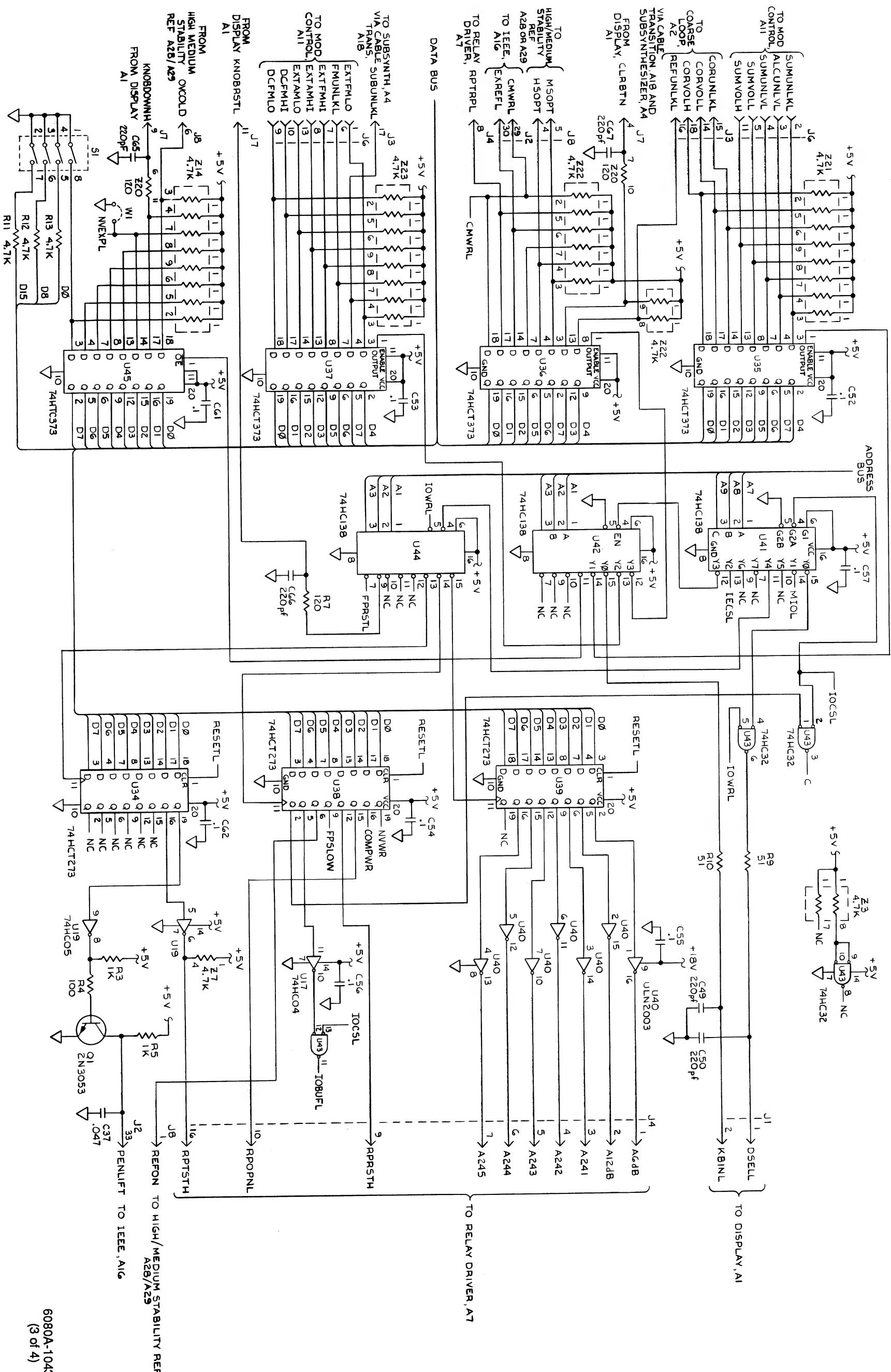


Figure 8-13. A13 Controller PCA (cont)

SCHEMATIC DIAGRAMS

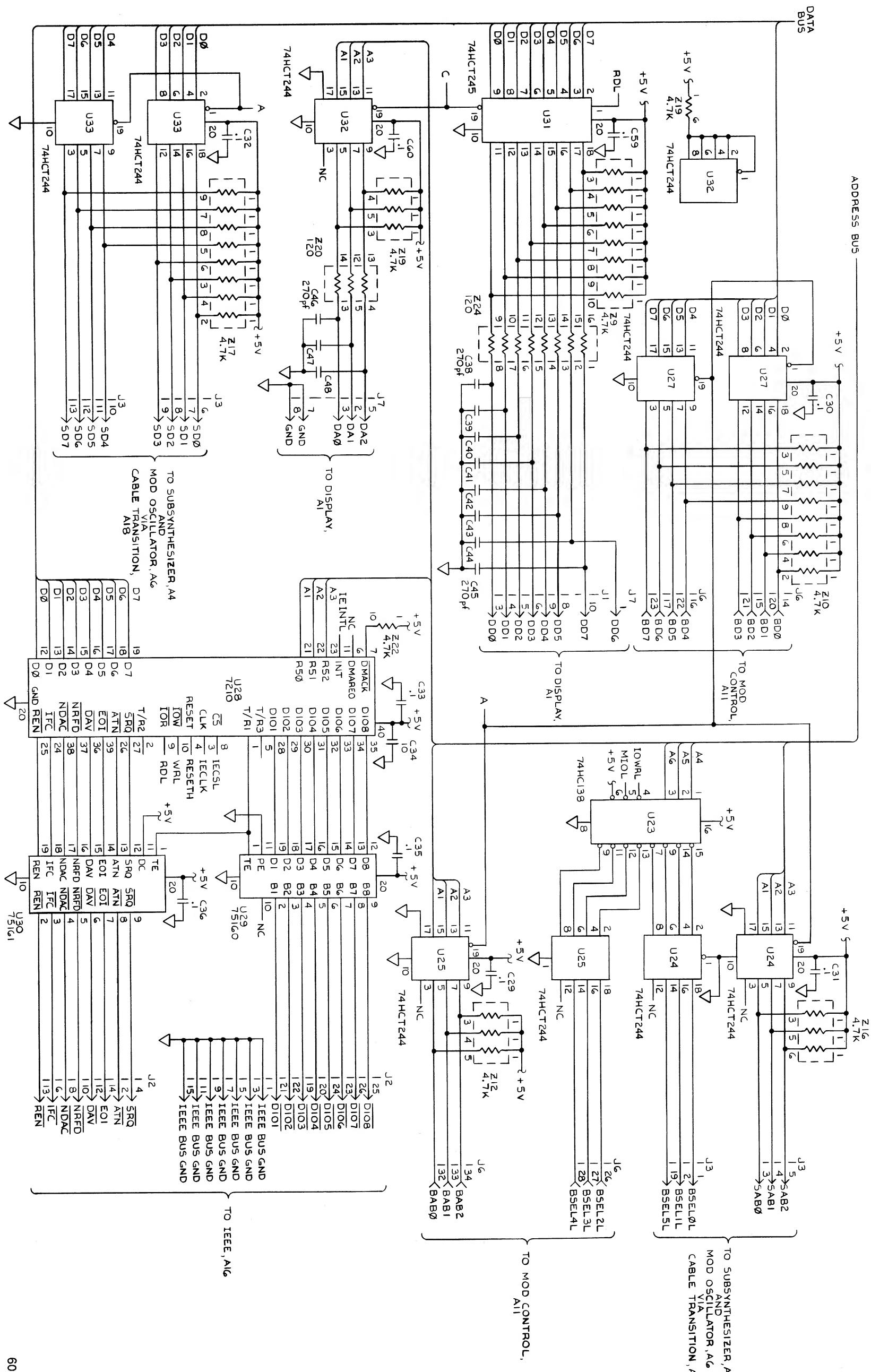


Figure 8-13. A13 Controller PCA (cont)

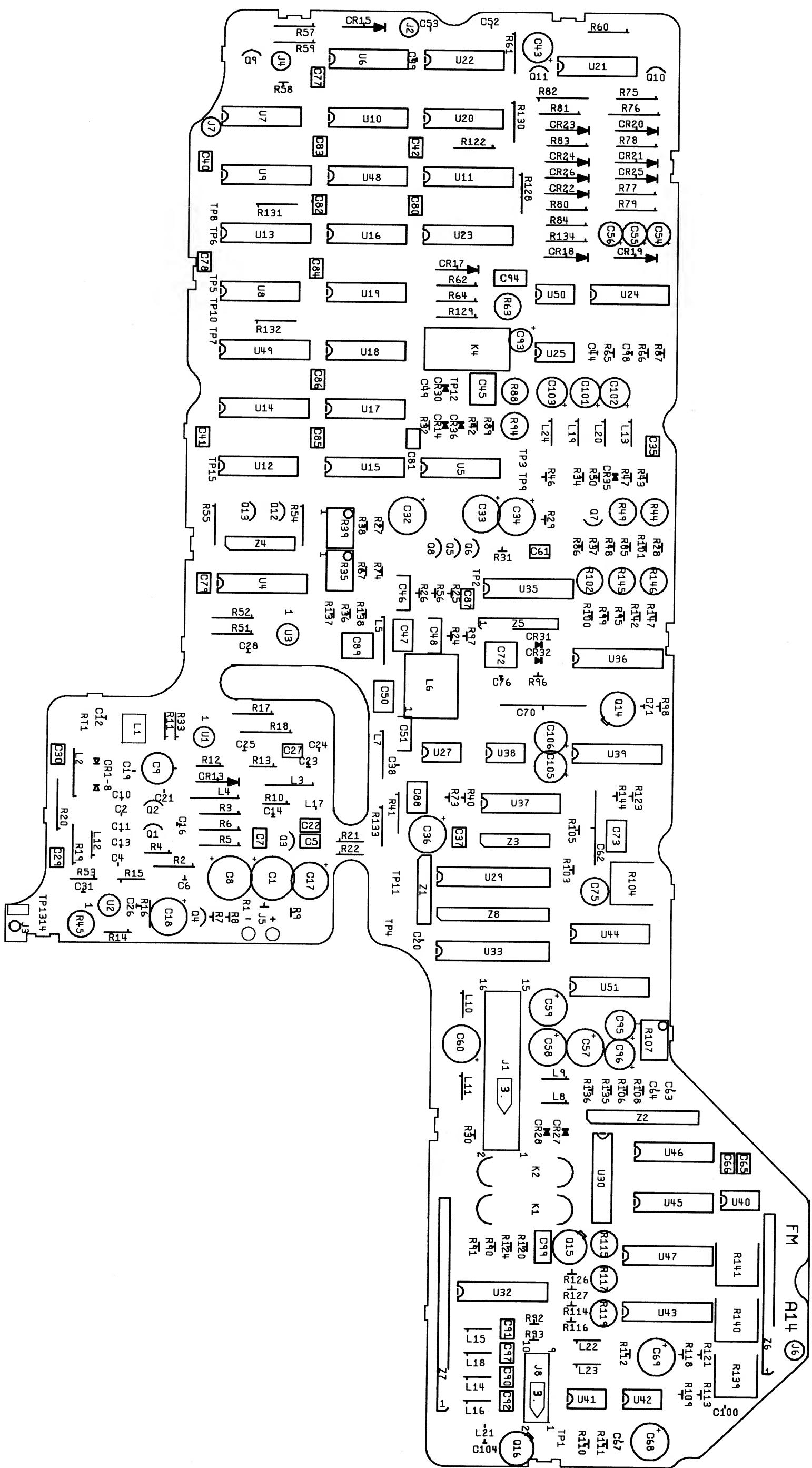


Figure 8-14. A14 FM PCA

Schematic Diagrams

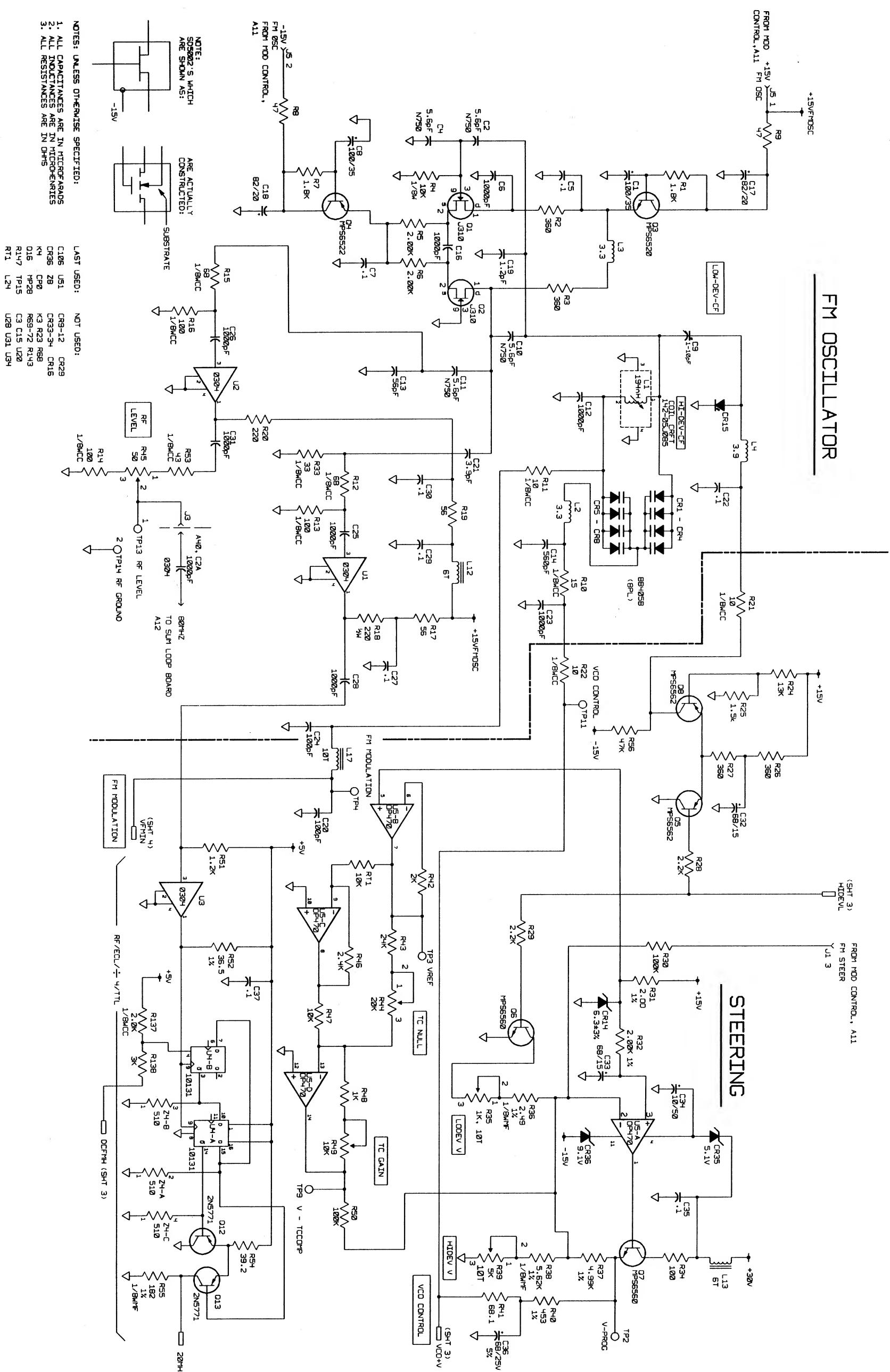


Figure 8-14. A14 FM PCA (cont)

6080A-1045
(1 of 4)

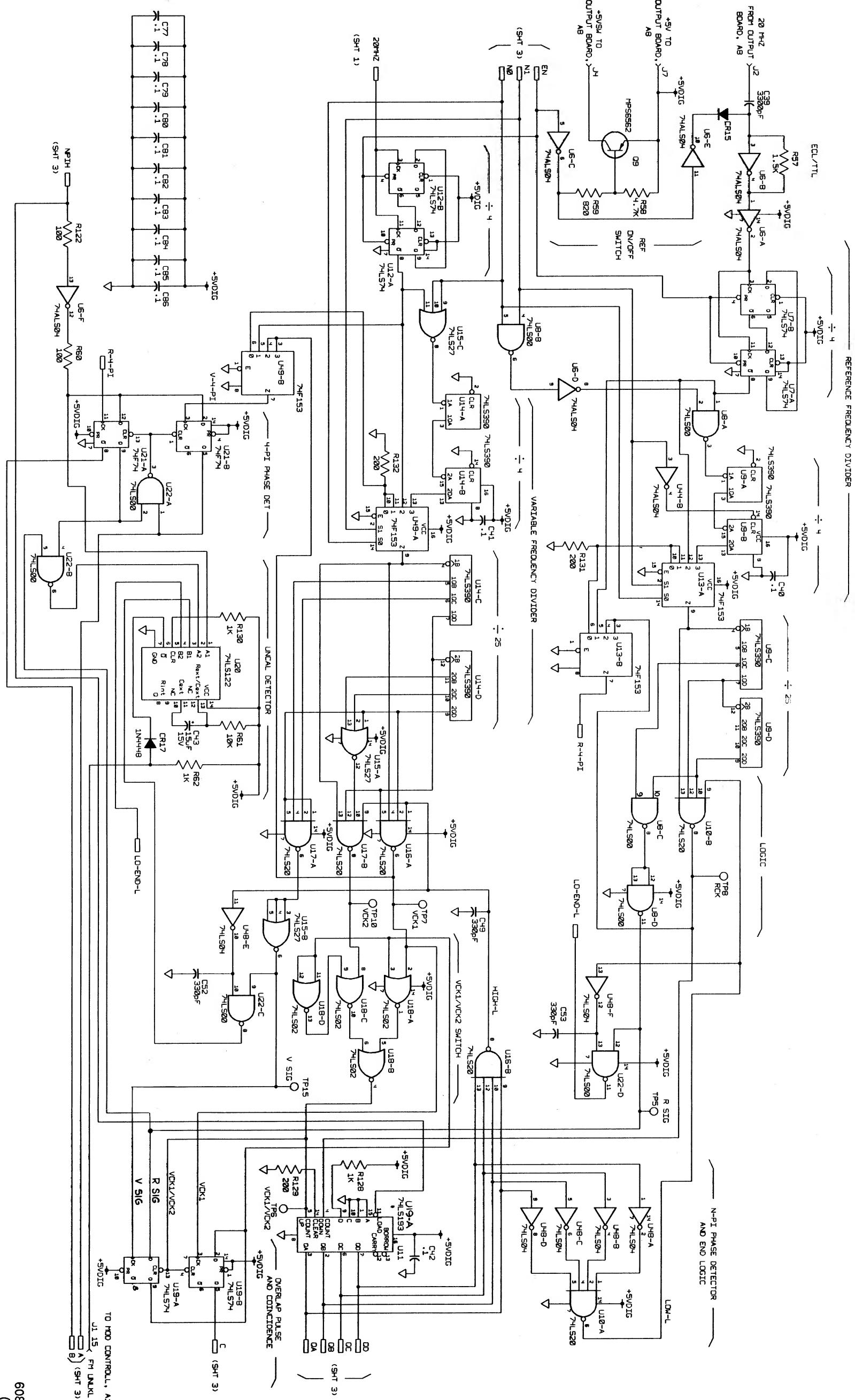


Figure 8-14. A14 FM PCA (cont)

SCHEMATIC DIAGRAMS

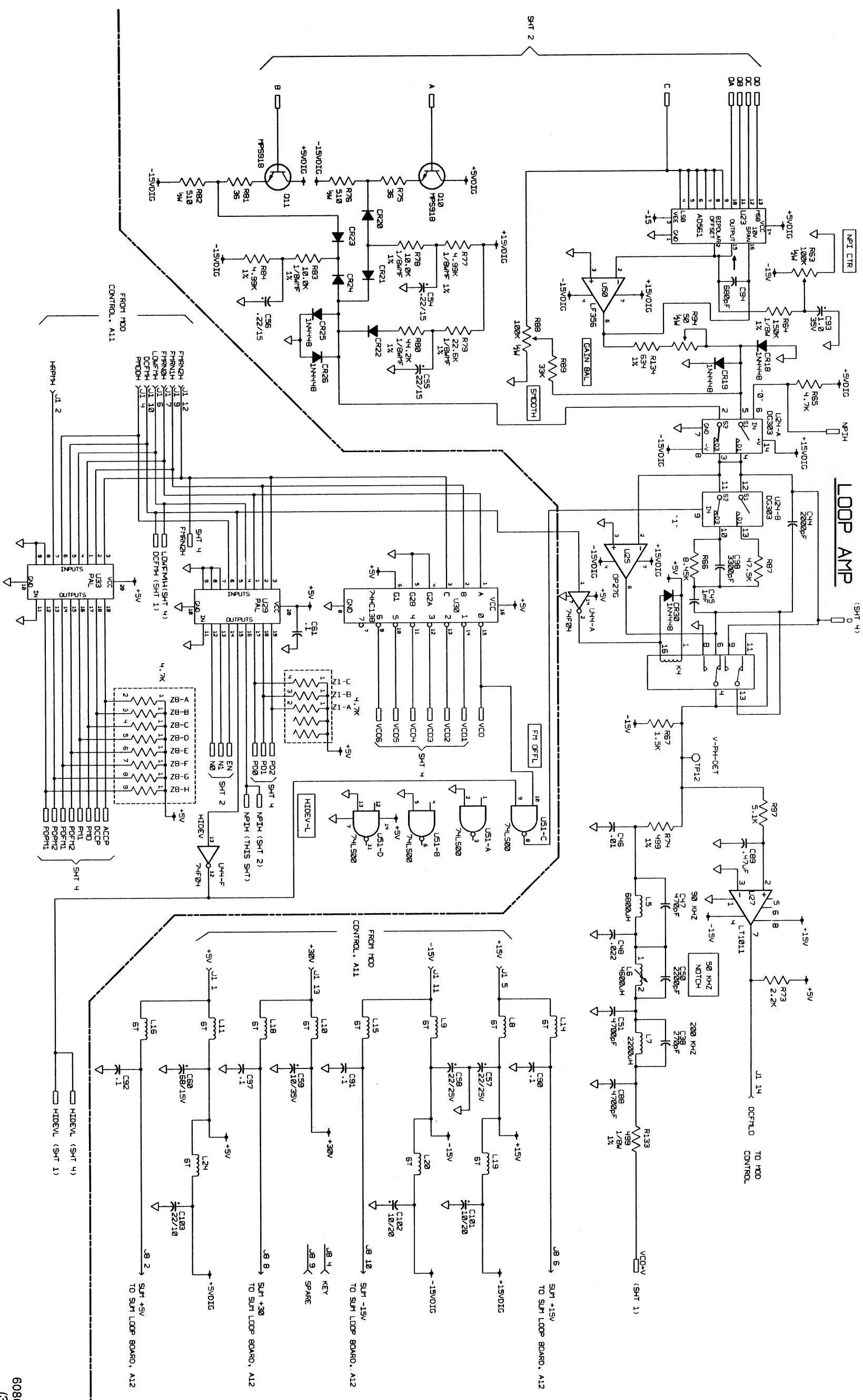
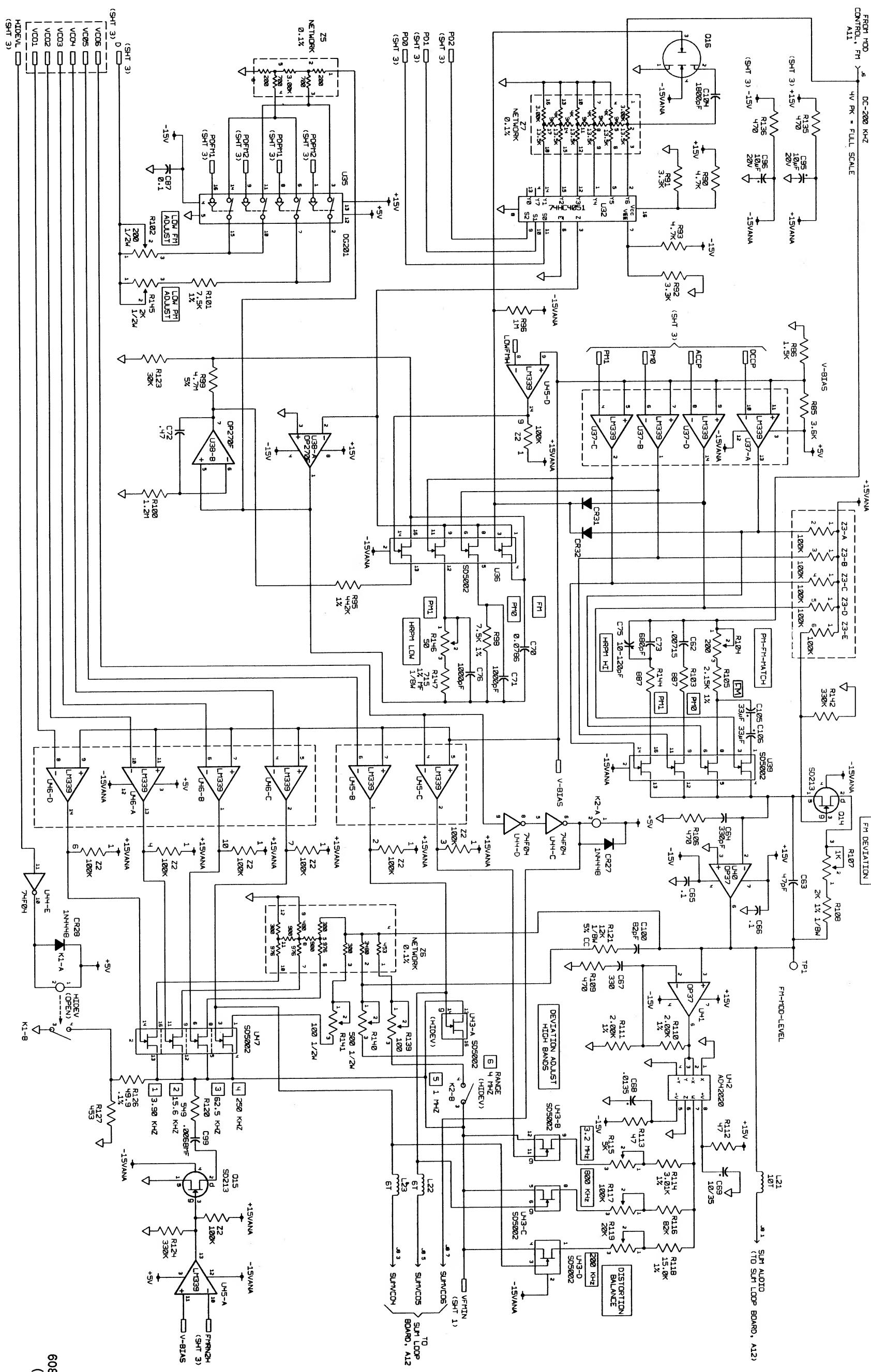


Figure 8-14. A14 FM PCA (cont)

Figure 8-14. A14 FM PCA (cont)



SCHEMATIC DIAGRAMS

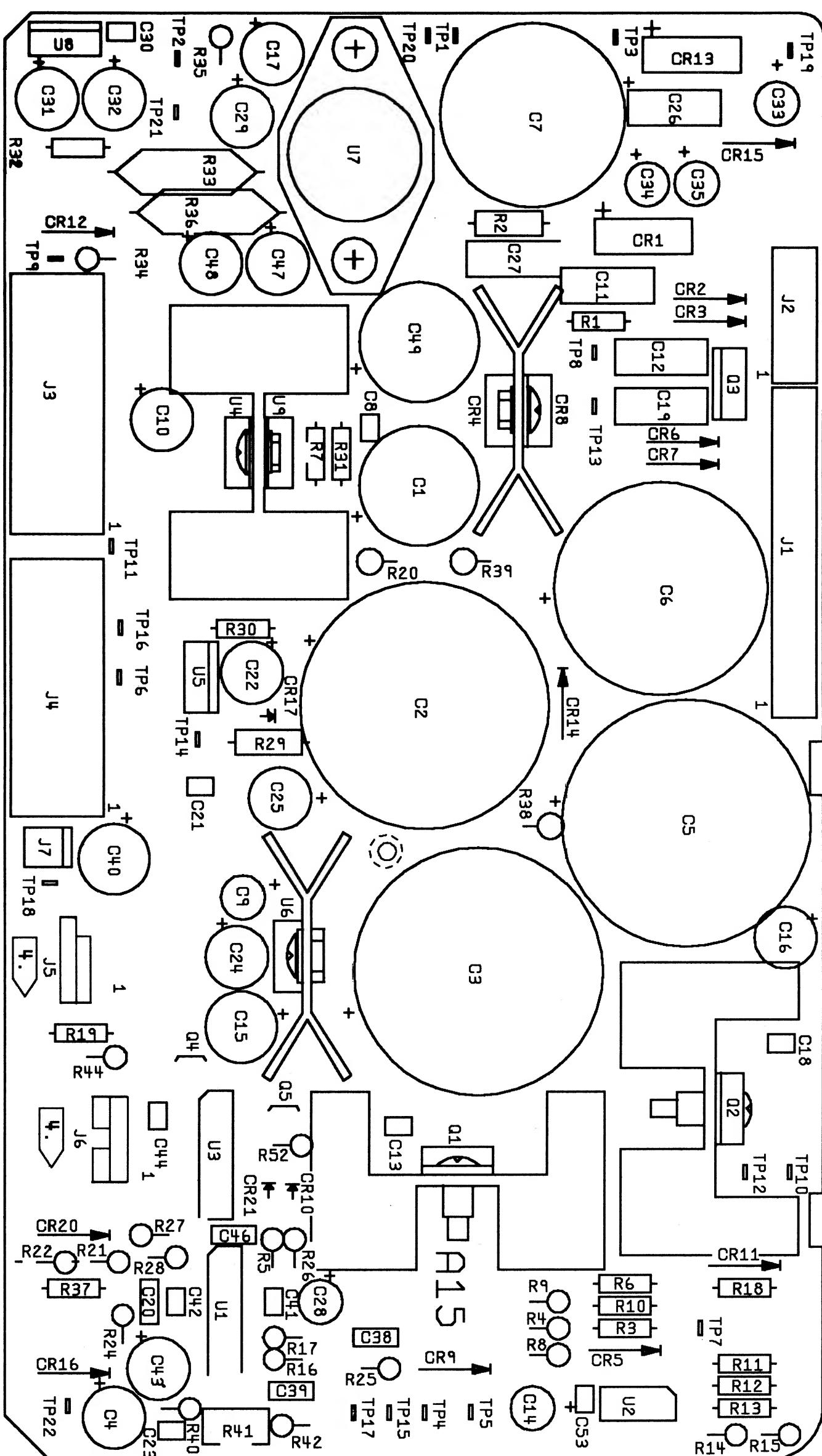


Figure 8-15. A15 Power Supply PCA

SCHEMATIC DIAGRAMS

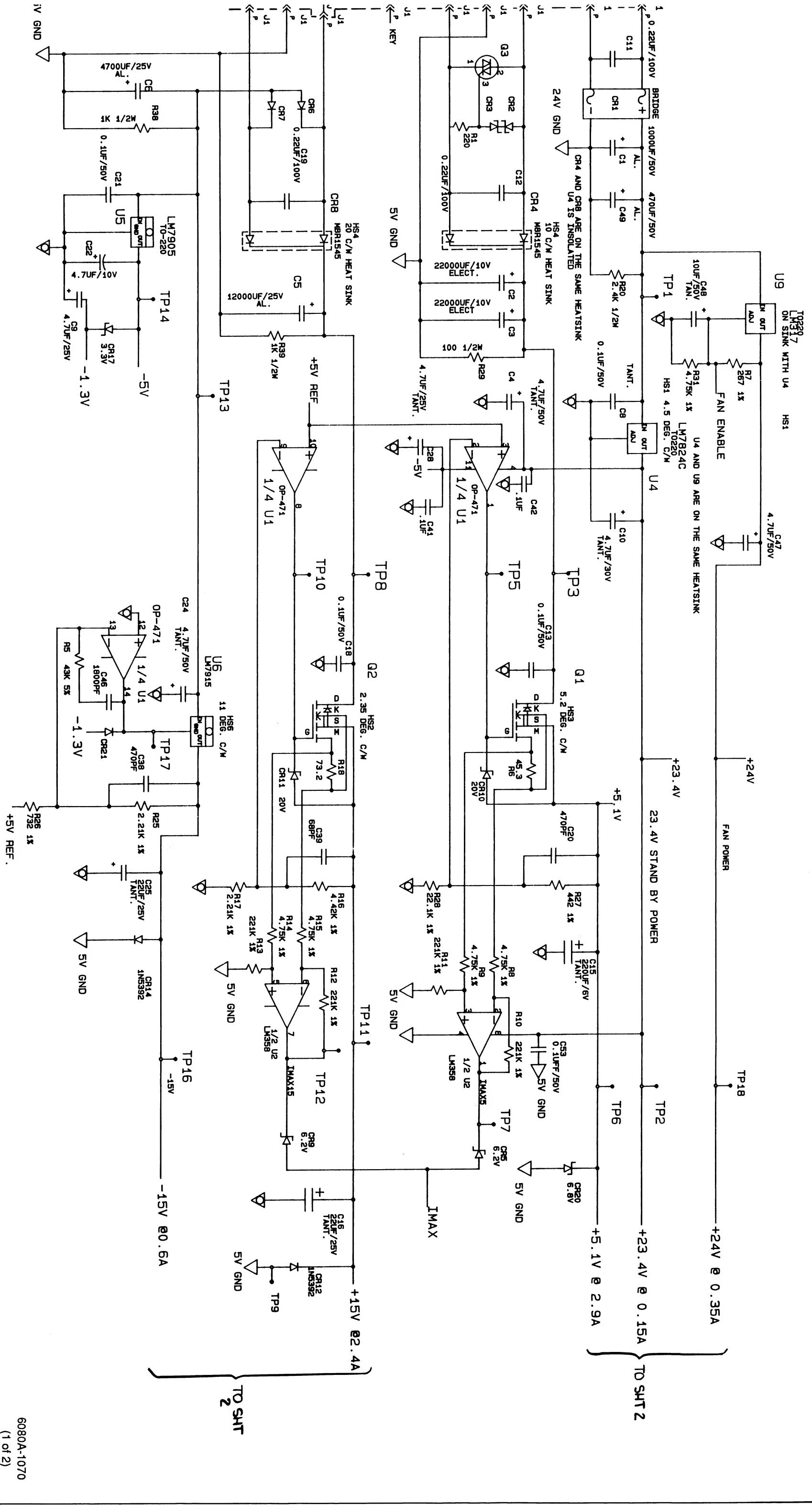
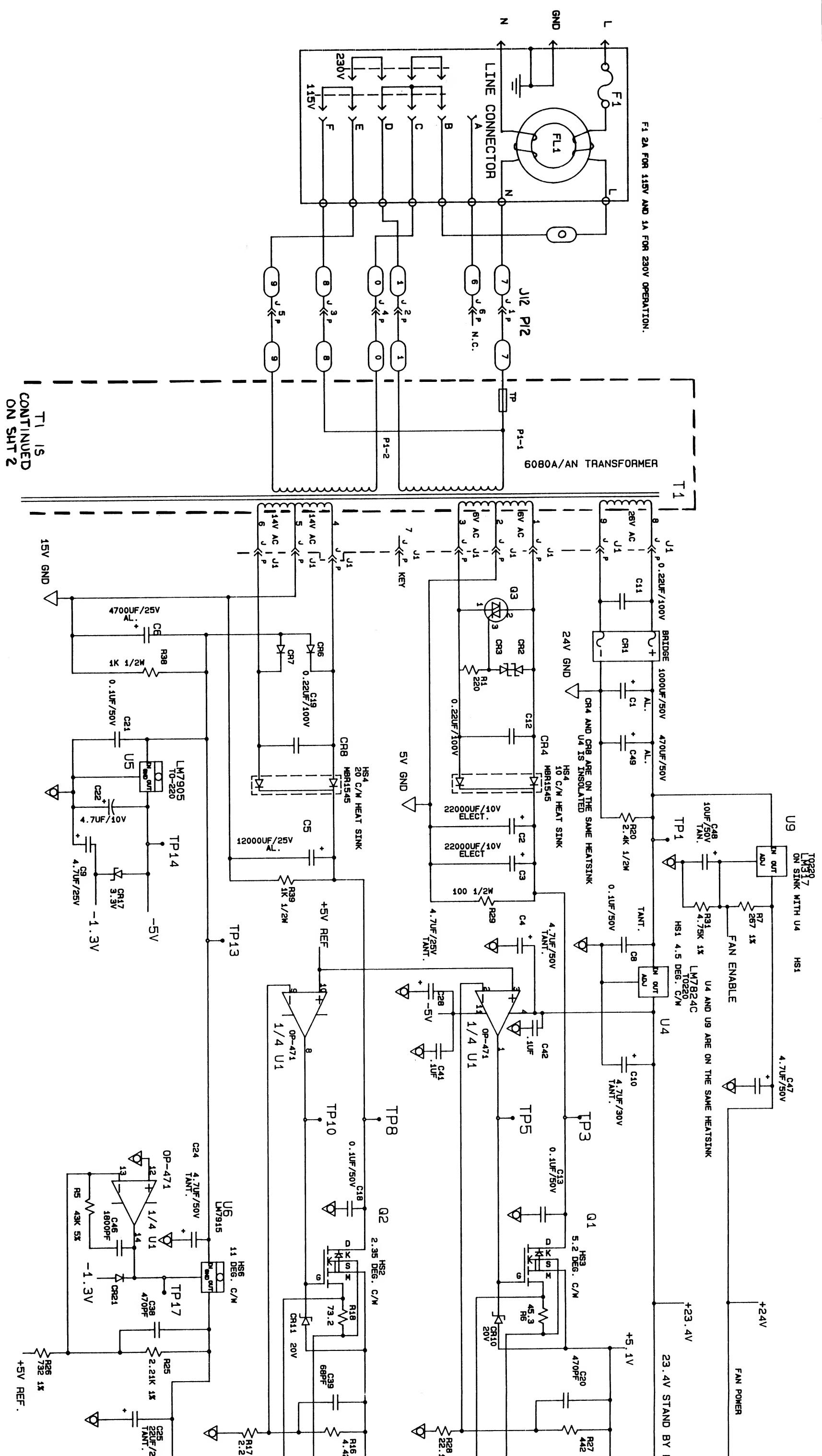


Figure 8-15. A15 Power Supply PCA (cont)



SCHEMATIC DIAGRAMS

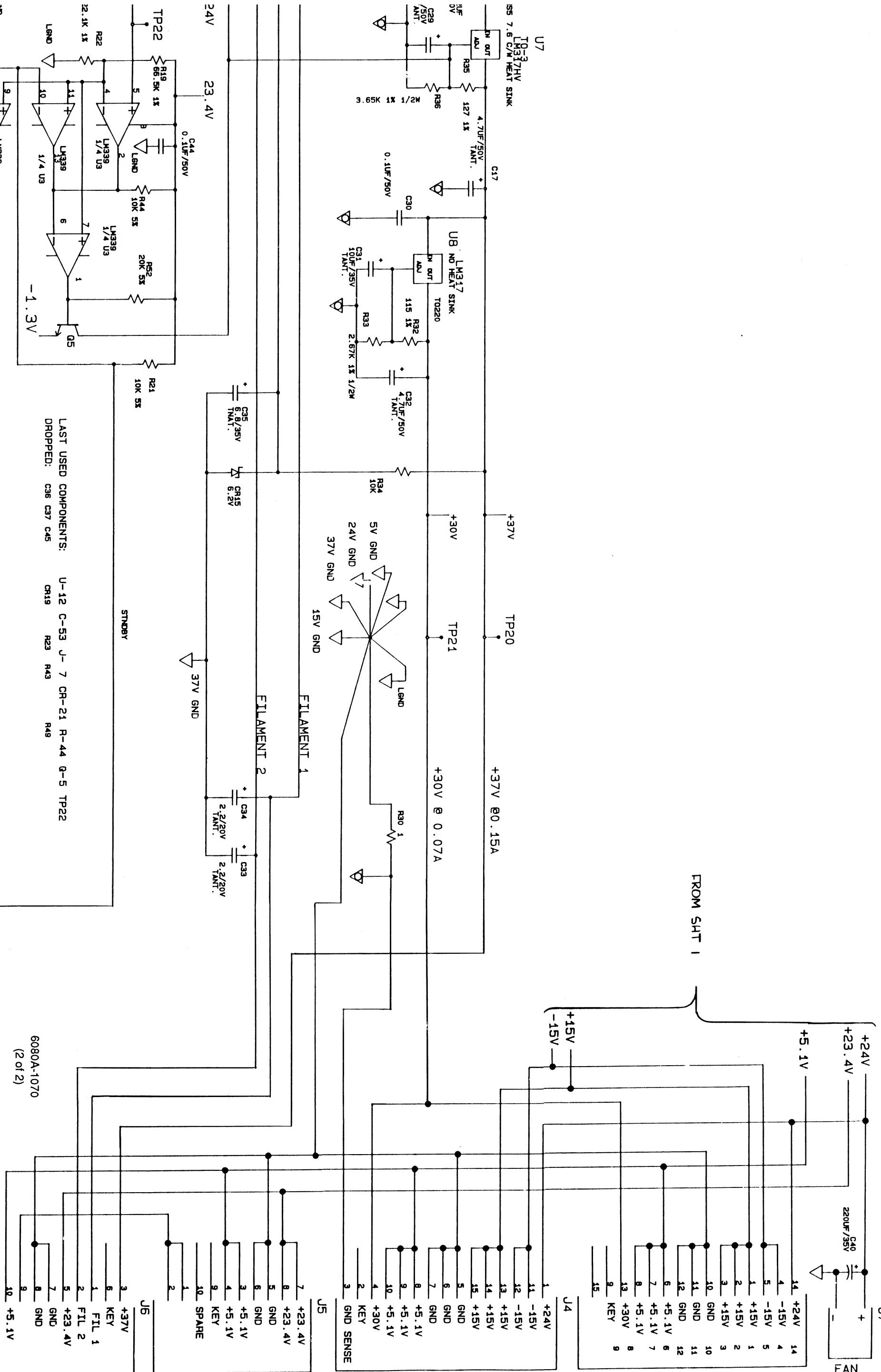
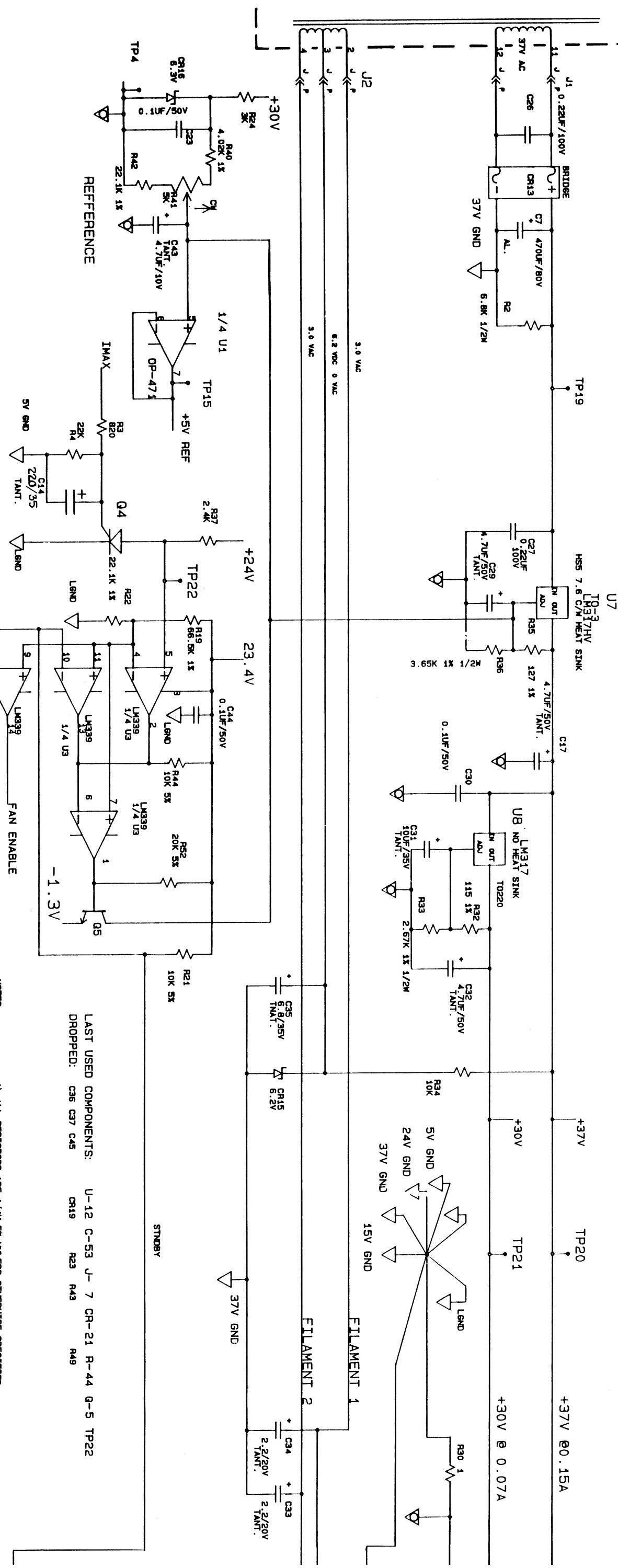


Figure 8-15. A15 Power Supply PCA (cont)

T1 (CONTINUED FROM
SHT I)



NOTES:

1) ALL RESISTORS ARE 1/4W 5% UNLESS OTHERWISE SPECIFIED

LAST USED COMPONENTS: U-12 C-53 J-7 CR-21 R-44 Q-5 TP22

DROPPED: C36 C37 C45 CR19 R23 R43 R49

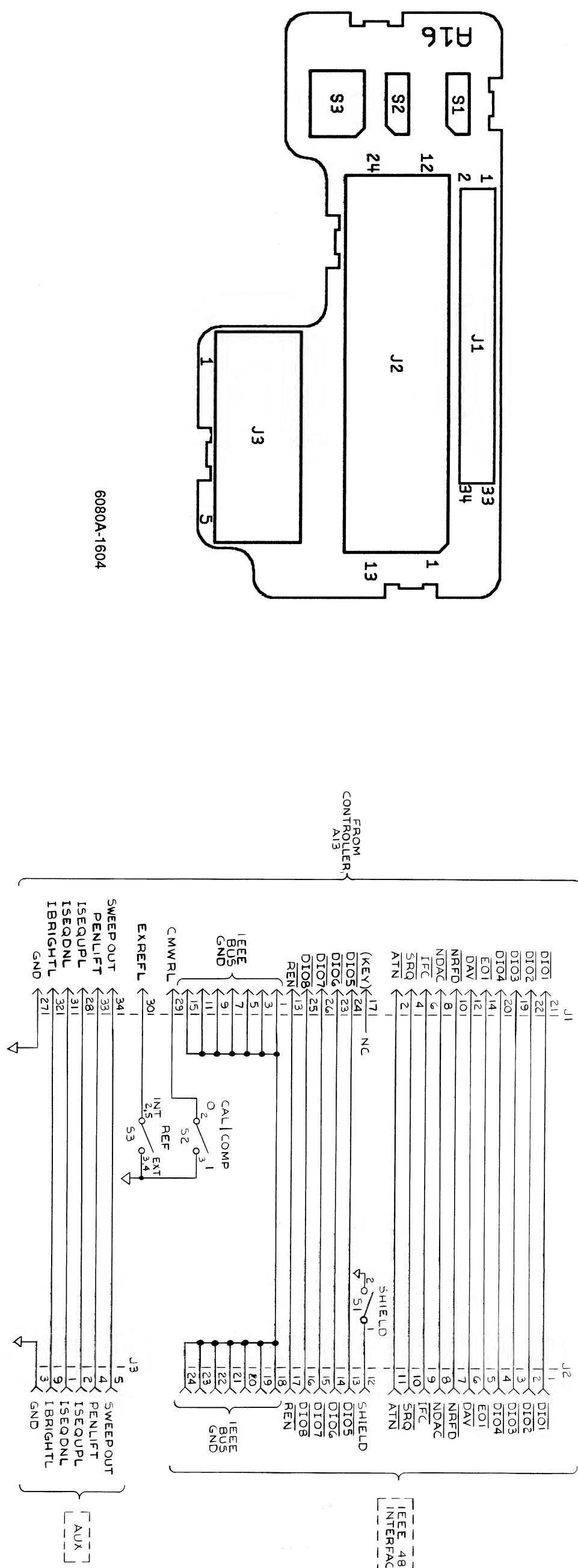
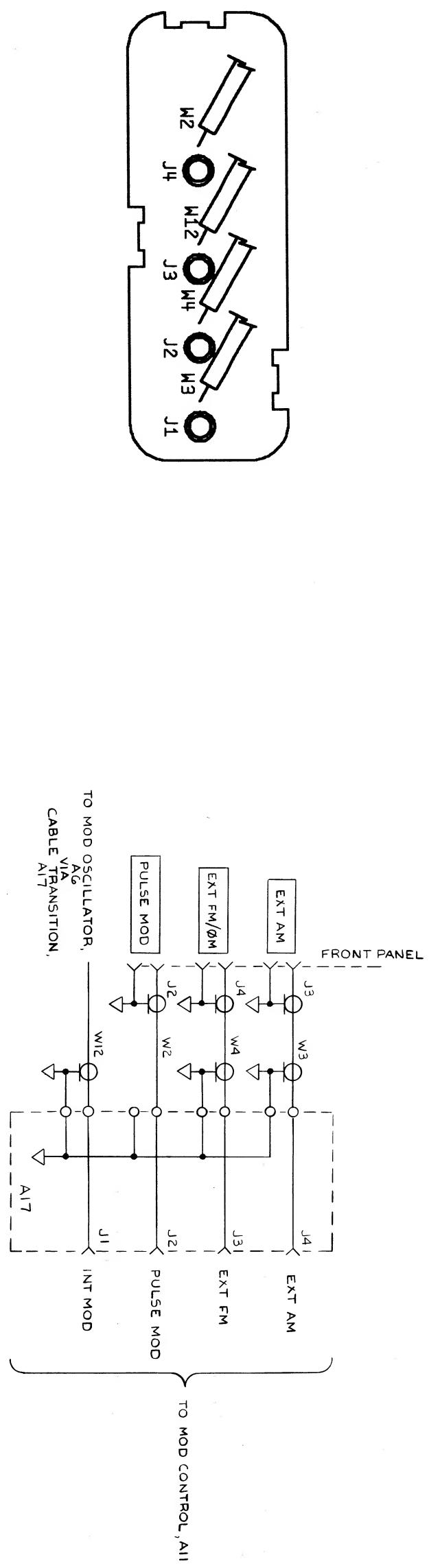


Figure 8-16. A16 IEEE Connector PCA

6080A-1071

SCHEMATIC DIAGRAMS



6080A-1610

6080A-1044

Figure 8-17. A17 Cable Transition PCA

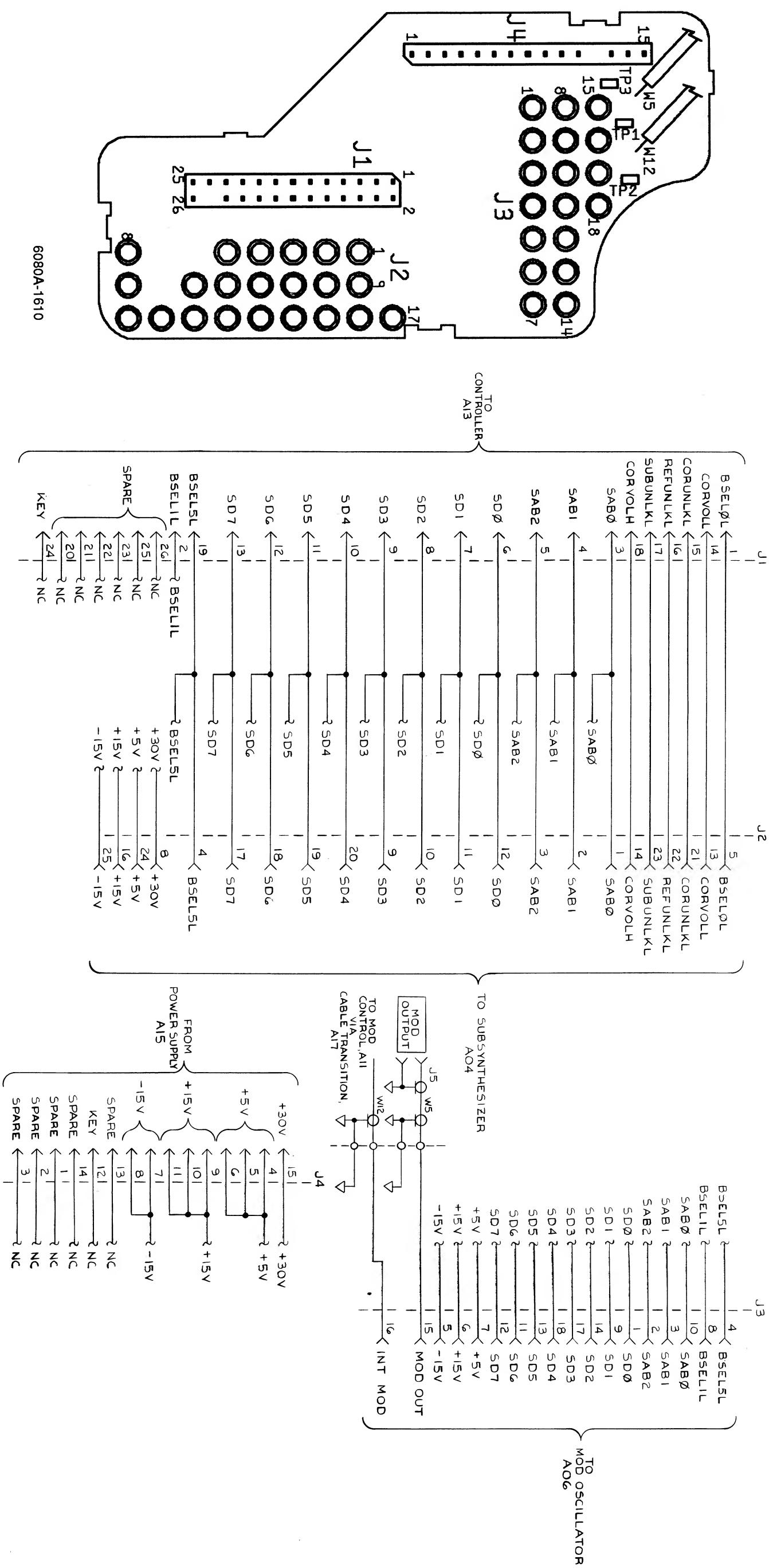


Figure 8-18. A18 Cable Transition PCA

SCHEMATIC DIAGRAMS

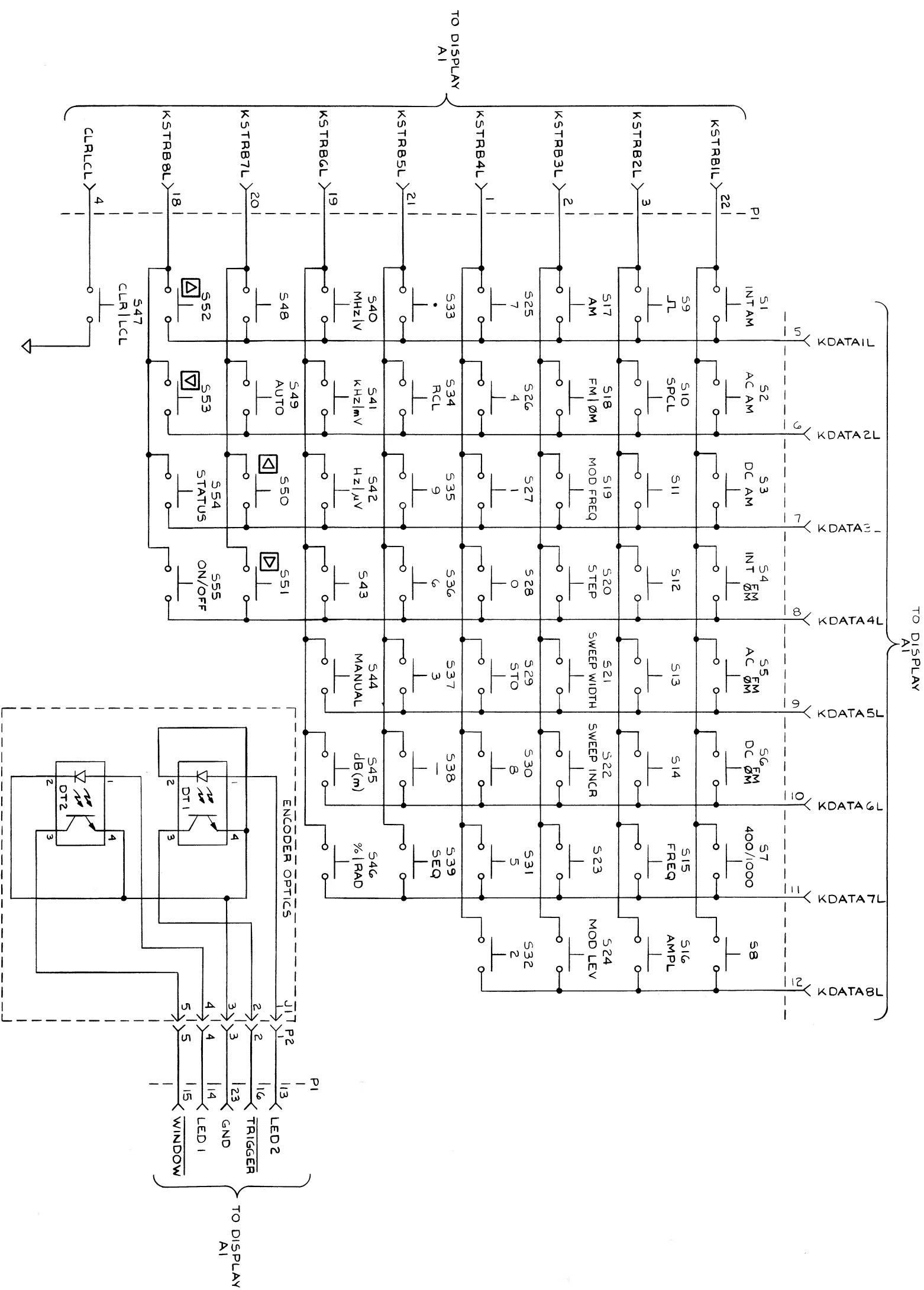
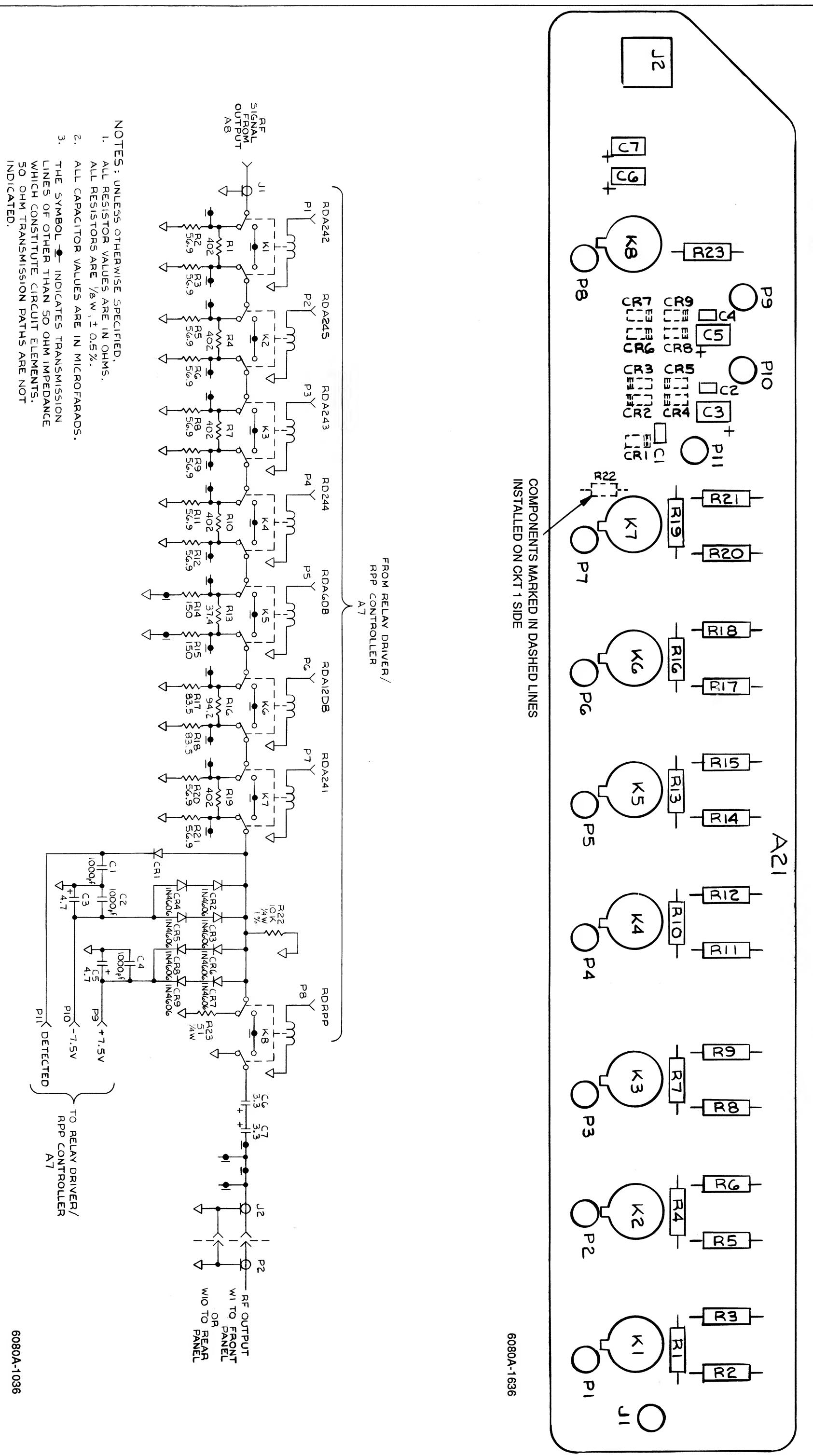


Figure 8-19. A19 Switch PCA



SCHEMATIC DIAGRAMS

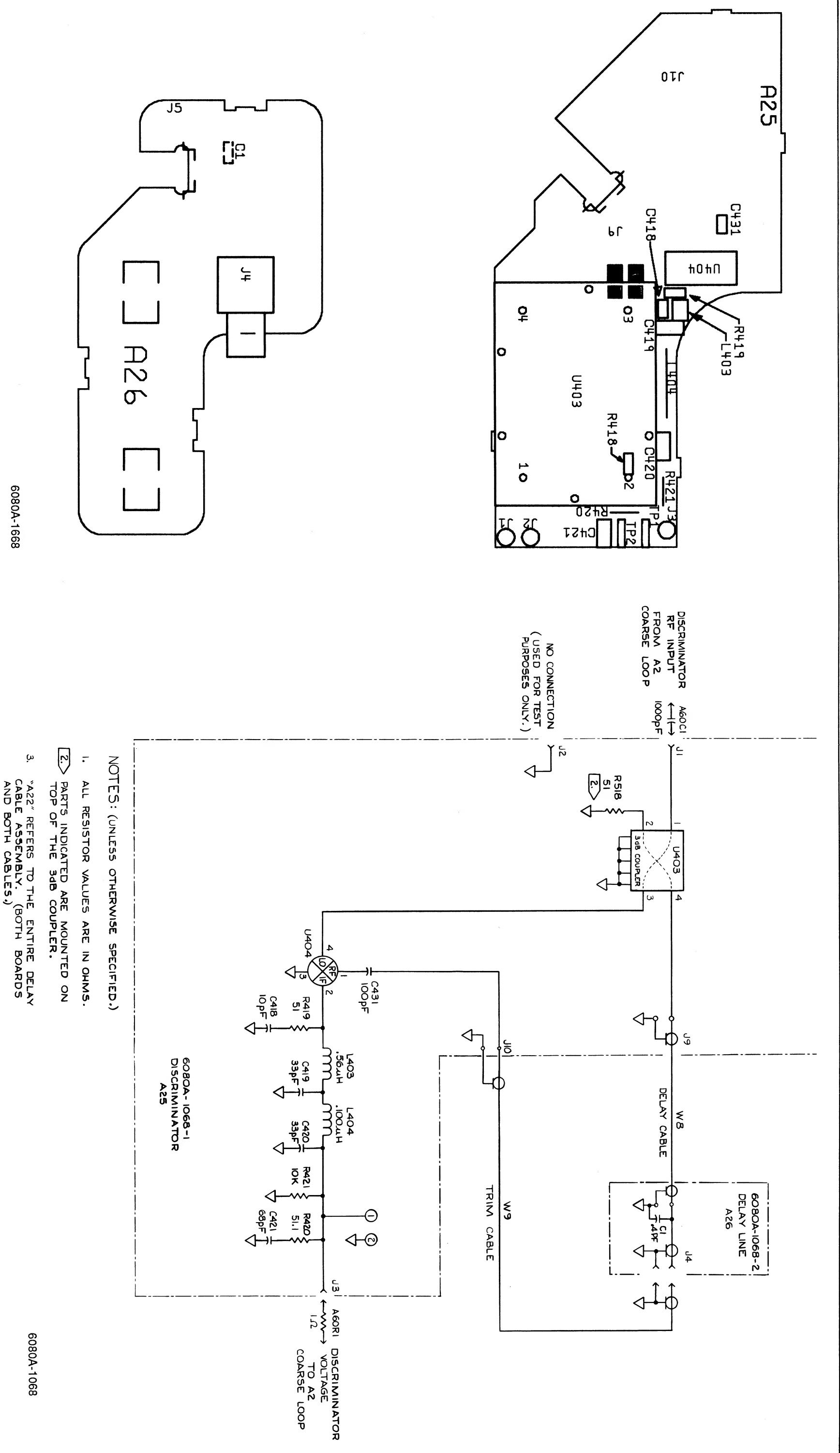


Figure 8-21. A22, A25, and A26 Delay Line/Discriminator PCA